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MACROMODULAR
COMPUTER DESIGN
PART 2
MANUFACTURING DESCRIPTION

VOLUME II
ELECTRONIC PACKAGE ASSEMBLY

Technical Report No. 31

FINAL REPORT - FEBRUARY, 1974

CONTRACT SD-302 (ARPA)

COMPUTER SYSTEMS LABORATORY

WASHINGTON UNIVERSITY

ST. LOUIS, MISSOURI

MACROMODULAR COMPUTER DESIGN
FINAL REPORT - CONTRACT SD-302
FEBRUARY, 1974

Technical Report No. 31

PART 2 - MANUFACTURING DESCRIPTION
VOL. II-ELECTRONICS PACKAGE ASSEMBLY

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The views and conclusions contained in this document are those of the authors and should not be interpreted as necessarily representing the official policies either expressed or implied, of the Advanced Research Projects Agency or the U.S. Government.

Computer Systems Laboratory
Washington University
St. Louis, Missouri

ABSTRACT

Manufacturing documents, including parts lists, assembly pictorials, and adjustment procedures for the LOGIC, ARITHMETIC, SHIFT, COMPARE and REGISTER macro-module electronic subassemblies are given.

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COMMON BOARDS

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LOGIC FUNCTION MODULE

PAGES 201.0D thru 201.9D3

ADDITION MODULE

PAGES 202.0D thru 202.7D2

SHIFT MODULE

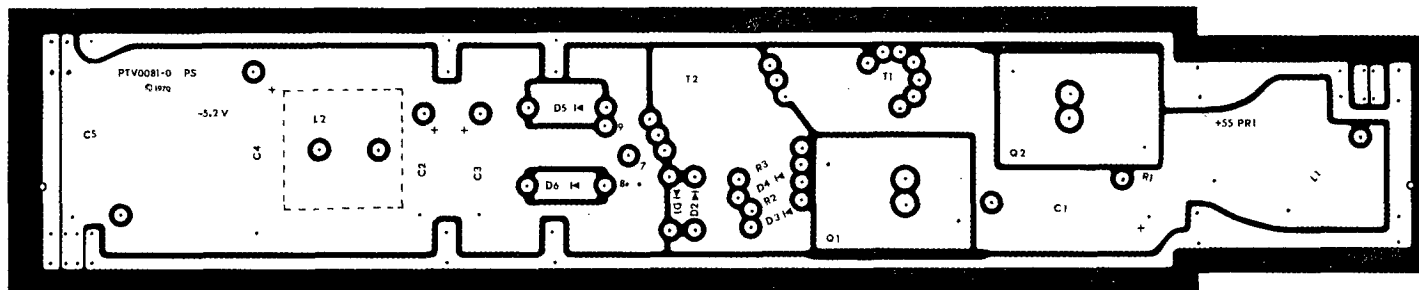
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COMPARE MODULE

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REGISTER MODULE

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				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION POWER SUPPLY BOARD ASSEMBLY				
				MACROMODULAR PROJECT		APPROVED BY FOR DATE Cam MANUF. 24 SEP 70 Cam MANUF. 7/15/71		ENG. TJC DRAWN BY PLC CHECKED <i>[Signature]</i>	DRAWING NO. 200.5DI DATE 9/23/70	
1	10-5-70	E.C.G. 0043								
CHANGE NO.	DATE	DESCRIPTION								

TRANSFORMERS

TWO REQUIRED

TYPE	LOCATION
MPS-T1	T1
* MPS-T2	T2

INDUCTORS

TWO REQUIRED

TYPE	LOCATION
MPS-L1	L1
MPS-L2	L2

TRANSISTORS

TWO REQUIRED

TYPE	LOCATION
RCA 40374	Q1
	Q2

DIODES

SIX REQUIRED

TYPE	LOCATION
MOTOROLA MR810	D1
	D2
	D3
	D4
MOTOROLA SR1922A	D5
	D6

CAPACITORS

FIVE REQUIRED

TYPE	LOCATION
SPRAGUE ALUMINUM 600D 256 G060 DD4	C1
SPRAGUE TANTALUM 150D 475X 0050 B2	C2
	C3
SPRAGUE TANTALUM 150D 187X 0006 R2	C4
SPRAGUE DISC CERAMIC C080A 120 P474Y OR C052B100P474Z	C5

RESISTORS

5% CARBON COMP.

THREE REQUIRED

TYPE	LOCATION
13 K OHM 1/2 W	R1
75 OHM 1/4 W	R2
	R3

CONNECTORS

17 REQUIRED

AMPMODU 85863-4

PRINTED CIRCUIT BOARD

ONE REQUIRED

PTV 0081-0

NOTE:

MPS-T2-78 USED IN FOLLOWING MODULES:

LOGIC
ADDITION
SHIFT
COMPARE
DECODE

MPS-T2-79 USED IN FOLLOWING MODULES:

REGISTER
LOAD
FUNCTION CALLER

MPS-T2-81 USED IN FOLLOWING MODULES:

CALL
MERGE/RENDEZVOUS
DATA BRANCH
INTERLOCK

5	2-9-73	E.C.O. 0284
4	11-15-72	E.C.O. 0275
3	7-28-71	E.C.O. 0219
2	4-20-71	E.C.O. 0172
1	10-5-70	E.C.O. 0043
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY		
WASHINGTON UNIVERSITY		
ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE		
PARTS LIST		
POWER SUPPLY BOARD		
APPROVED		ENG. TJC
BY	FOR	DATE
Com	MANUF.	24 SEP 70
DRAWN BY		DRAWING NO.
CDG		200.5D2
CHECKED		DATE
TJC		9/23/70

PSS-1

COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY

INDUCTOR DESCRIPTION AND SPECIFICATION

Part Number MPS-L1

Inductor Description:

The inductor is a single layer coil of 35 ± 1 turns of number 24 magnet wire wound on a *Magnetics Incorporated permalloy power toroidal core number 55118-A2. The magnet wire is insulated with polyurethane (sodereze or equal) for 90°C operation.

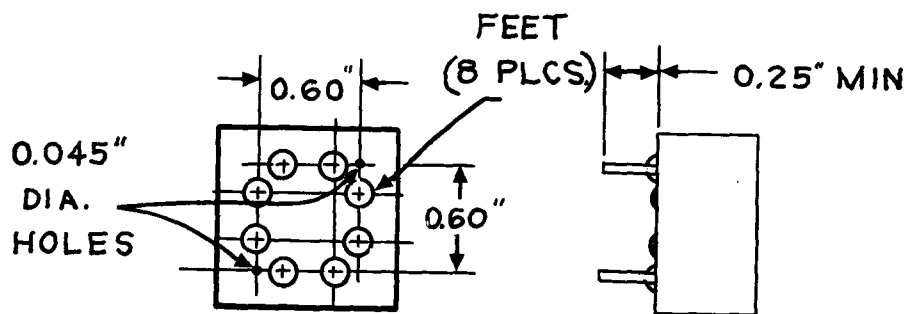
The wound inductor is potted in a **Milton Ross shell 50071 and header 60200 with Scotcheast number 222 polyurethane resin. The two leads of the wound inductor extend through the header as shown in the figure. The leads extend through the header a minimum of 0.25 inches and, beginning at a distance of 0.05 inch from the package, are tinned.

The characters "MPS-L1" in white at least 0.1 inch high appear on the top of the package.

Finished Inductor Specifications:

I. Identification: The characters "MPS-L1" in white at least 0.1 inch high will appear on the top of the inductor package and shall remain readable after hard rubbing with thumb.

II. Inductance: The inductance of the inductor shall be $L_p > 100 \mu\text{H}$ and $Q > 8$ when measured on a General Radio type 1650-A impedance bridge. The bridge controls are set to: OSC LEVEL control adjusted for maximum output; the function switch to INT 1 KC; the CRL SELECTOR to L_p ; and the CRL MULTIPLIER to $100 \mu\text{H}$.



BOTTOM VIEW

*Magnetics Incorporated
Butler, Pa. 16001

**Milton Ross Company
511 Second Street Pike Box 158
Southampton, Pa. 18966

PSS-2

COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY

INDUCTOR DESCRIPTION AND SPECIFICATION

Part Number MPS-L2

200.5D2C

Inductor Description:

The inductor is a single layer coil of 20 ± 1 turns of number 20 magnet wire wound on a *Magnetics Incorporated permalloy power toroidal core number 55118-A2. The magnet wire is insulated with polyurethane (sodereze or equal) for 90°C operation.

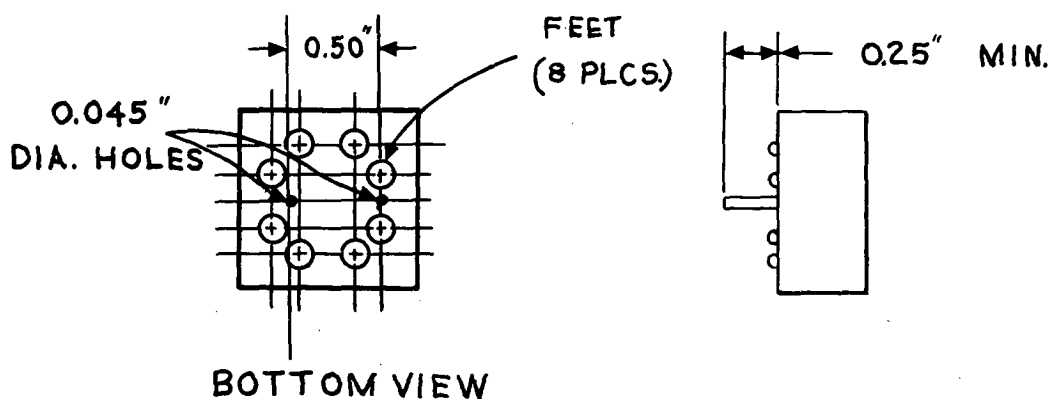
The wound inductor is potted in a **Milton Ross shell 50071 and header 60200 with Scotcheast number 222 polyurethane resin. The two leads of the wound inductor extend through the header as shown in the figure. The leads extend through the header a minimum of 0.25 inches and, beginning at a distance of 0.05 inch from the package, are tinned.

The characters "MPS-L2" in white, at least 0.1 inch high, appear on the top of the package.

Finished Inductor Specifications:

I. Identification: The characters "MPS-L2" in white at least 0.1 inch high will appear on the top of the inductor package and shall remain readable after hard rubbing with thumb.

II. Inductance: The inductance of the inductor shall be $L_p > 30 \mu\text{H}$ and $Q > 8$ when measured on a General Radio type 1650-A impedance bridge. The bridge controls are set to: OSC LEVEL control adjusted for maximum output; the function switch to INT 1 KC; the CRL SELECTOR to L_p ; and the CRL MULTIPLIER to $100 \mu\text{H}$.



*Magnetics Incorporated
Butler, Pa. 16001

**Milton Ross Company
511 South Street Pike Box 158
Southampton, Pa. 18966

PSS-3

COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY

TIMING TRANSFORMER DESCRIPTION AND SPECIFICATION

Part Number MPS-T1

Transformer Description:

The transformer core is an *Indiana General toroidal ferrite core number CF-102-C2 that has been coated with paint to reduce scratching of the wire wound on the core. Windings T1 and T2 of Figure 1 form an 16 turn bifilar winding of number 30 copper wire which is uniformly distributed around core. Windings T3 and T4 form a second bifilar winding of 3 turns of number 30 copper wire which is uniformly distributed around the core. The copper wire insulation will be polyurethane-nylon cover coat (Nyleze or better for 125°C operation).

The wound transformer is installed in a **Epoxy Products Company shell 173-02-10-43A and header 068-02-10-43A. Two of the 9 pins in the header 068-02-10-43A are cut off above and below the header per Figure 2. A piece of number 3 mylar tape or equivalent is placed over the cut pins to protect the transformer windings from scratches. The transformer leads are soldered to the header pins per Figures 1 and 3. The pin numbers used in Figure 3 are the same as the lead numbers of Figure 1. The assembled transformer is filled with Scotcheast number 212 red potting compound.

A sample of 5 Indiana General cores from each shipment received by the vendor shall be forwarded to the Computer Components Laboratory for tests before the transformers are assembled.

Finished Transformer Specifications:

I. Identification: The characters "MPS-T1" at least 0.1 inches high will appear on the transformer and shall remain readable after hard rubbing with thumb.

II. Turns: The number of turns on each winding must be exact:

- a. T1 = 16 turns
- b. T2 = 16 turns
- c. T3 = 3 turns
- d. T4 = 3 turns

*Indiana General
Electronics Div/Ferrites
Keasbey, New Jersey

**Epoxy Products Company
Div of Allied Products Corp
119 Coit Street
Irvington, New Jersey

III. High Voltage Breakdown: Less than 0.1 milliamperes shall flow when 200 V_{RMS} AC is applied for one minute across:

- a. pins 1, 2 and 3 shorted together and pins 4 and 5 shorted together.
- b. pins 1, 2 and 3 shorted together and pins 6 and 7 shorted together.
- c. pins 4 and 5 shorted together and pins 6 and 7 shorted together.

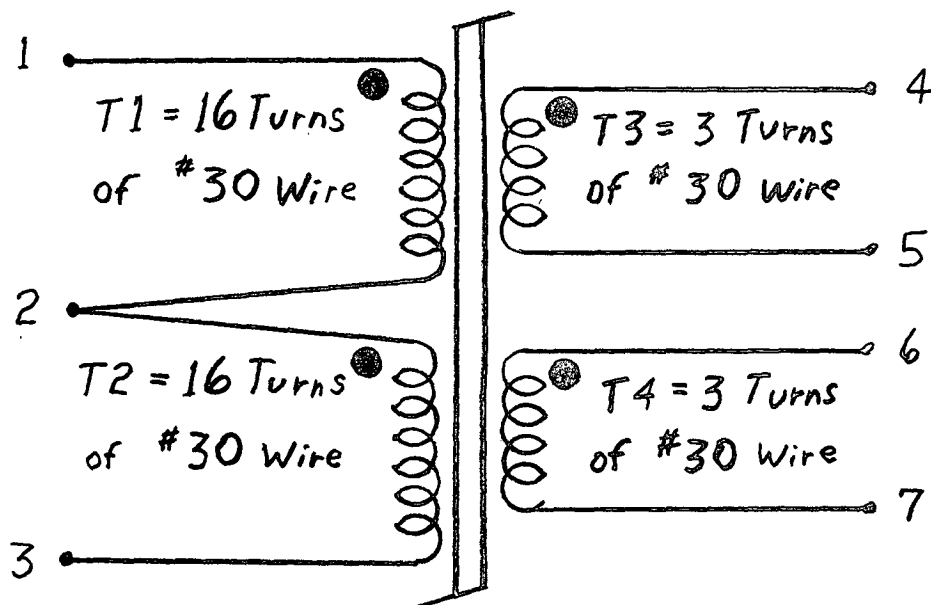


Figure 1

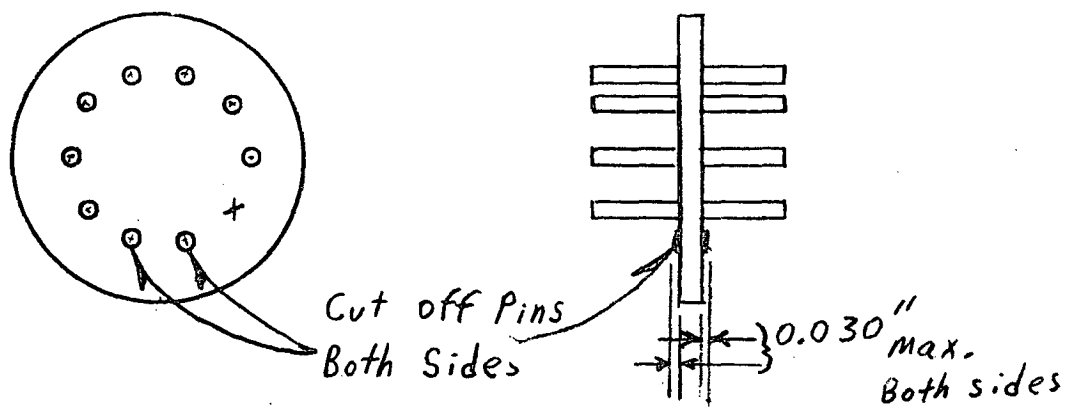
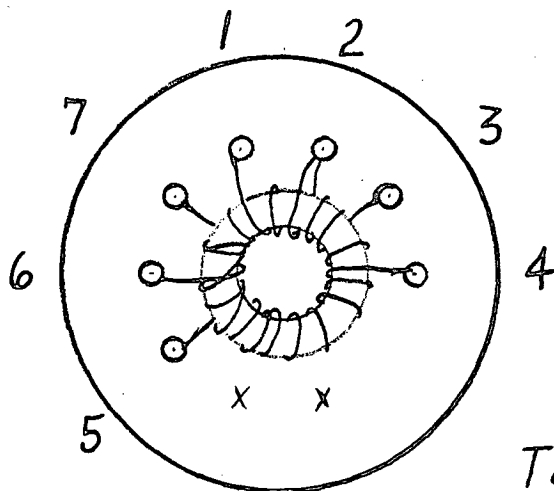


Figure 2



Top View Before
Potting

Figure 3

PSS-4

COMPUTER SYSTEMS LABORATORY

WASHINGTON UNIVERSITY

POWER TRANSFORMER DESCRIPTION AND SPECIFICATION

Part Number MPS-T2-78

200.5D2I

Transformer Description:

The coil form is a *Ferroxcube type number 2616PCB1 printed circuit bobbin.

Windings T1 and T2 are first wound on the bobbin as a 78 turn bifilar winding of number 30 copper wire uniformly covering the length of the bobbin. Next, windings T3 and T4 are wound on the bobbin as a 6 turn bifilar winding of number 30 copper wire. A single layer of tape (Mylar or equivalent) less than 0.007 inch thick is then placed over the windings. Windings T5 and T6 are then wound on the bobbin as a 9 turn bifilar winding of number 20 copper wire. The wire insulation shall be polyurethane nylon overcoat (Nyleze) or better for 120°C operation.

Windings T1, T2, T3 and T4 are connected to the bobbins' pins as shown in Figures 1 and 2. The ends of windings T5 and T6 extend out the side shown in Figure 2 and are 0.7 to 1.0 inch long. Wires 8A and 8B must be identified together. Wires 7 and 9 need not be individually identified.

The wound bobbin is then placed inside two *Ferroxcube number 2616P-L00-3B7 flat ground half-pot cores. The cores are then cemented together, with the center posts of the cores aligned, using Biggs R-312 epoxy cement. The two slits in the side of the transformer are both completely filled with a rigid epoxy such as **Mista Pox 103. The completed transformer is then sprayed with a light coat of clear varnish.

Finished Transformer Specifications:

I. Identification: The characters "MPS-T2-78" at least 0.1 inches high will appear on the transformer and shall remain readable after hard rubbing with thumb.

*Ferroxcube Corporation
Saugerties, New York

**M and R Plastics and Coatings, Inc.
11460 Dorsett Road
Maryland Heights, Mo. 63042

II. Turns: The number of turns on each winding must be exact:

- a. T1 = 78 turns
- b. T2 = 78 turns
- c. T3 = 6 turns
- d. T4 = 6 turns
- e. T5 = 9 turns
- f. T6 = 9 turns

III. Inductance: The inductance between pins 1 and 2 shall be $L_p > 16 \text{ mH}$ and $Q > 15$ with all other windings open circuited when measured on a General Radio type 1650-A impedance bridge. The bridge controls are set to: OSC LEVEL control adjusted for maximum output; the function switch to INT 1 KC; the CRL SELECTOR to L_p ; and the CRL MULTIPLIER to 10 mH.

IV. High Voltage Breakdown: Less than 0.1 milliamperes shall flow when 200 V_{RMS} AC is applied for one minute across pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

Less than 0.1 milliamperes shall flow when 500 V_{RMS} AC is applied for one minute across:

- a. pins 1, 2, 3, 4, 5 and 6 shorted together and wires 7, 8A, 8B and 9 shorted together.
- b. pins 1, 2, 3, 4, 5 and 6 with wires 7, 8A, 8B and 9 all shorted together and the core.

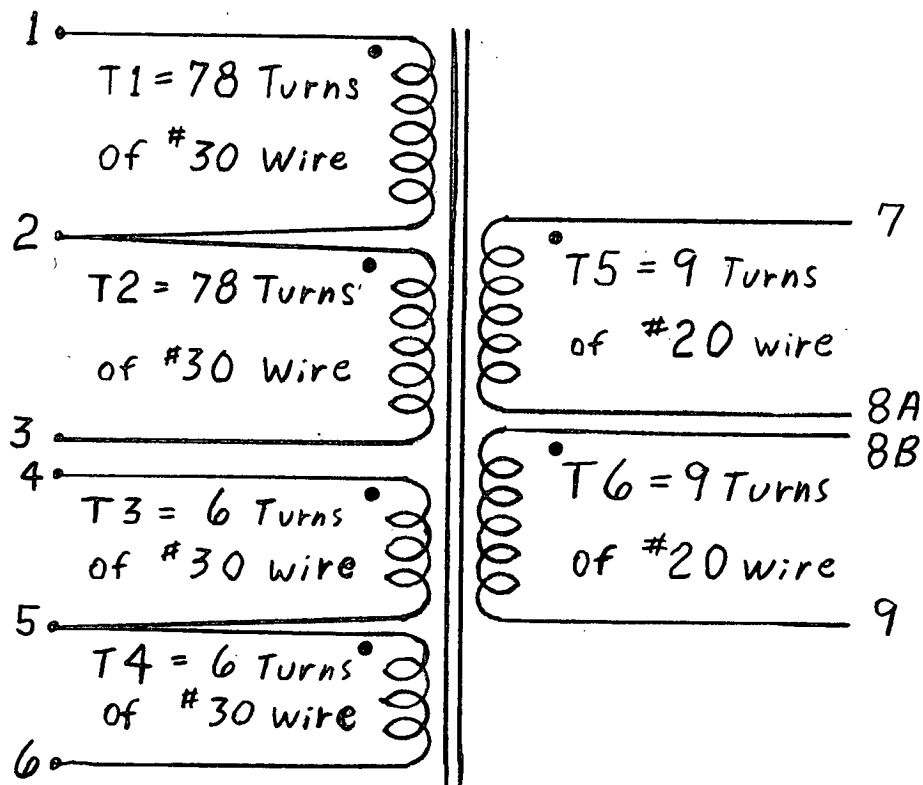


Figure 1

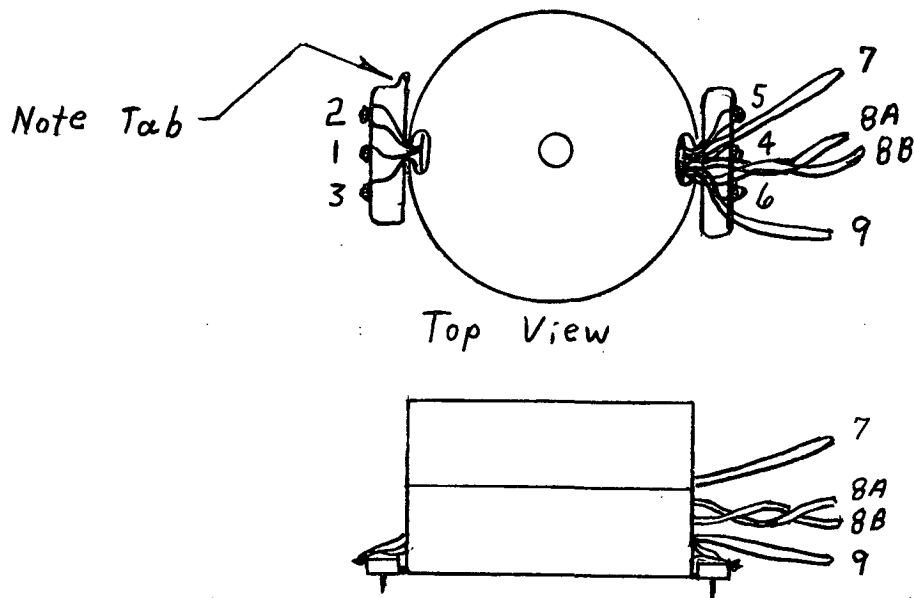


Figure 2

PSS-5

COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY

POWER TRANSFORMER DESCRIPTION AND SPECIFICATION

Part Number MPS-T2-79

Transformer Description:

The coil form is a *Ferroxcube type number 2616PCB1 printed circuit bobbin. Windings T1 and T2 are first wound on the bobbin as a 79 turn bifilar winding of number 30 copper wire uniformly covering the length of the bobbin. Next, windings T3 and T4 are wound on the bobbin as a 6 turn bifilar winding of number 30 copper wire. A single layer of tape (Mylar or equivalent) less than 0.007 inch thick is then placed over the windings. Windings T5 and T6 are then wound on the bobbin as a 9 turn bifilar winding of number 20 copper wire. The wire insulation shall be polyurethane nylon overcoat (Nyleze) or better for 120°C operation.

Windings T1, T2, T3 and T4 are connected to the bobbins' pins as shown in Figures 1 and 2. The ends of windings T5 and T6 extend out the side shown in Figure 2 and are 0.7 to 1.0 inch long. Wires 8A and 8B must be identified together. Wires 7 and 9 need not be individually identified.

The wound bobbin is then placed inside two *Ferroxcube number 2616P-L00-3B7 flat ground half-pot cores. The cores are then cemented together, with the center posts of the cores aligned, using Biggs R-312 epoxy cement. The two slits in the side of the transformer are both completely filled with a rigid epoxy such as **Mista Pox 103. The completed transformer is then sprayed with a light coat of clear varnish.

Finished Transformer Specifications:

I. Identification: The characters "MPS-T2-79" at least 0.1 inches high will appear on the transformer and shall remain readable after hard rubbing with thumb. The style "9" used shall be easily distinguished from an "8".

*Ferroxcube Corporation
Saugerties, New York

**M and R Plastics and Coatings, Inc.
11460 Dorsett Road
Maryland Heights, Mo. 63042

II. Turns: The number of turns on each winding must be exact:

- a. T1 = 79 turns
- b. T2 = 79 turns
- c. T3 = 6 turns
- d. T4 = 6 turns
- e. T5 = 9 turns
- f. T6 = 9 turns

III. Inductance: The inductance between pins 1 and 2 shall be $L_p > 16 \text{ mH}$ and $Q > 15$ with all other windings open circuited when measured on a General Radio type 1650-A impedance bridge. The bridge controls are set to: OSC LEVEL control adjusted for maximum output; the function switch to INT 1 KC; the CRL SELECTOR to L_p ; and the CRL MULTIPLIER to 10 mH.

IV. High Voltage Breakdown: Less than 0.1 milliamperes shall flow when 200 V_{RMS} AC is applied for one minute across pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

Less than 0.1 milliamperes shall flow when 500 V_{RMS} AC is applied for one minute across:

- a. pins 1, 2, 3, 4, 5 and 6 shorted together and wires 7, 8A, 8B and 9 shorted together.
- b. pins 1, 2, 3, 4, 5 and 6 with wires 7, 8A, 8B and 9 all shorted together and the core.

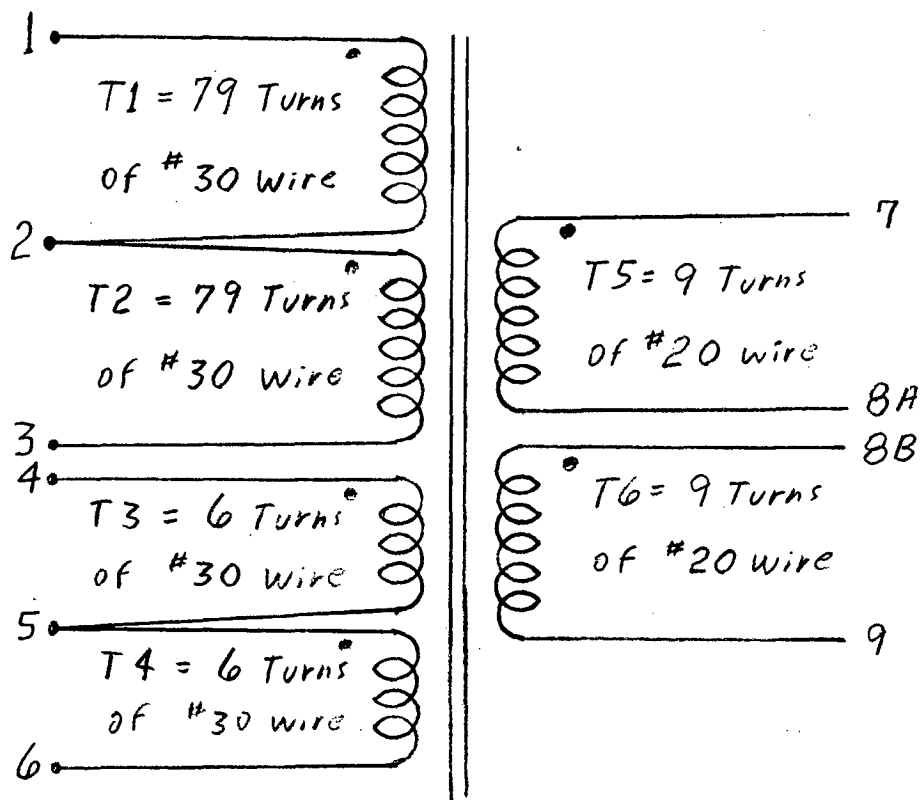


Figure 1

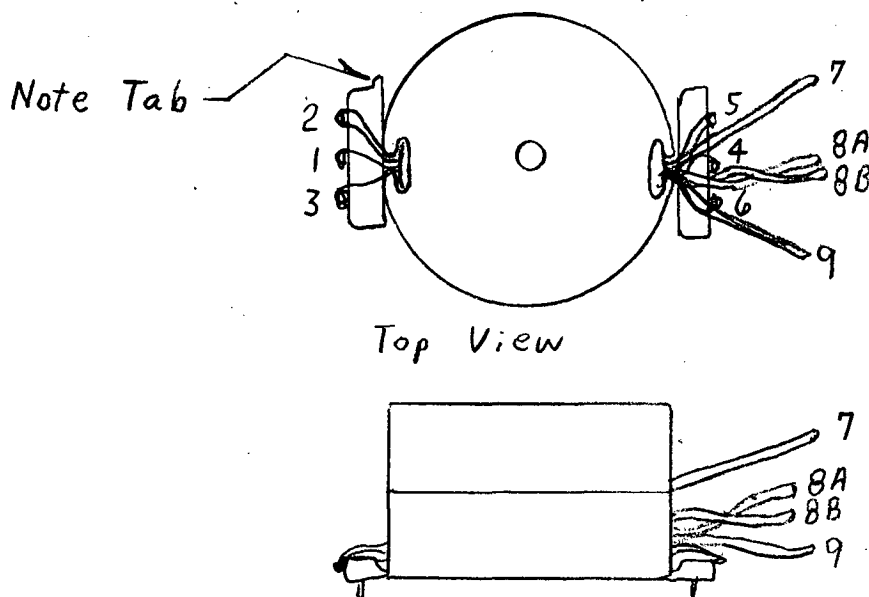


Figure 2

PSS-8

COMPUTER SYSTEMS LABORATORY

WASHINGTON UNIVERSITY

POWER TRANSFORMER DESCRIPTION AND SPECIFICATION

Part Number MPS-T2-81

200.5D2Q

Transformer Description:

The coil form is a *Ferroxcube type number 2616PCB1 printed circuit bobbin. Windings T1 and T2 are first wound on the bobbin as an 81 turn bifilar winding of number 30 copper wire uniformly covering the length of the bobbin. Next, windings T3 and T4 are wound on the bobbin as a 6 turn bifilar winding of number 30 copper wire. A single layer of mylar tape 0.003 to 0.007 inch thick is then placed over the windings. Windings T5 and T6 are then wound on the bobbin as a 9 turn bifilar winding of number 22 copper wire. The wire insulation shall be polyurethane nylon overcoat (Nyleze) for 120°C operation.

Windings T1, T2, T3, and T4 are connected to the bobbins' pins as shown in Figures 1 and 2. The ends of windings T5 and T6 extend out the side shown in Figure 2 and are 0.7 to 1.0 inch long. Wires 8A and 8B must be identified together. Wires 7 and 9 need not be individually identified.

The wound bobbin is then placed inside two *Ferroxcube number 2616-L00-3B7 flat ground half-pot cores. The cores are then cemented together, with the center posts of the cores aligned, using Biggs R-312 epoxy cement. The two slits in the side of the transformer are both completely filled with a rigid epoxy such as **Mista Pox 103. The completed transformer is then sprayed with a light coat of clear varnish.

Finished Transformer Specifications:

I. Identification: The characters "MPS-T2-81" at least 0.1 inches high will appear on the transformer and shall remain readable after hard rubbing with thumb. The style "8" used shall be easily distinguished from a "9".

*Ferroxcube Corporation
Saugerties, New York

**M and R Plastics and Coatings, Inc.
11460 Dorsett Road
Maryland Heights, Mo. 63042

II. Turns: The number of turns on each winding must be exact:

- a. T1 = 81 turns
- b. T2 = 81 turns
- c. T3 = 6 turns
- d. T4 = 6 turns
- e. T5 = 9 turns
- f. T6 = 9 turns

III. Inductance: The inductance between pins 1 and 2 shall be $L_p > 16 \text{ mH}$ and $Q > 15$ with all other windings open circuited when measured on a General Radio type 1650-A impedance bridge. The bridge controls are set to: OSC LEVEL control adjusted for maximum output; the function switch to INT 1 KC; the CRL SELECTOR to L_p ; and the CRL MULTIPLIER to 10 mH.

IV. High Voltage Breakdown: Less than 0.1 milliamperes shall flow when 200 V_{RMS} AC is applied for one minute across pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

Less than 0.1 milliamperes shall flow when 500 V_{RMS} AC is applied for one minute across:

- a. pins 1, 2, 3, 4, 5 and 6 shorted together and wires 7, 8A, 8B and 9 shorted together.
- b. pins 1, 2, 3, 4, 5 and 6 with wires 7, 8A, 8B and 9 all shorted together and the core.

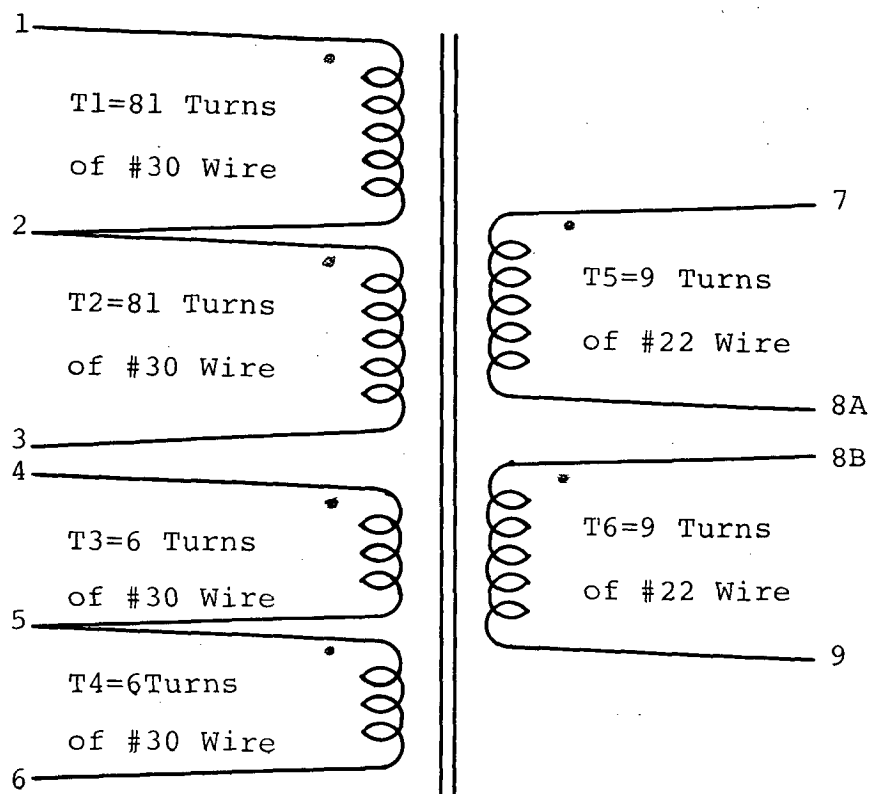
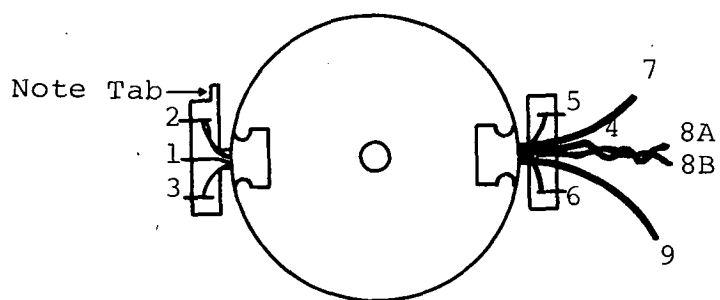


Figure 1



Top View

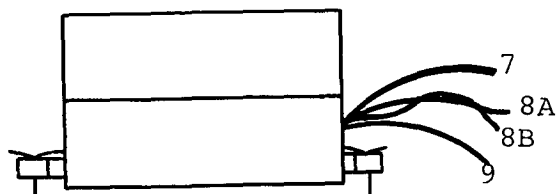
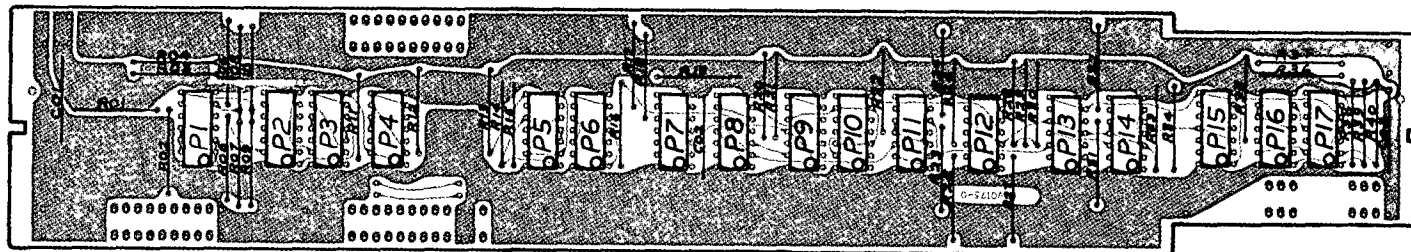


Figure 2

NOTE: INSTALL FEMALE AMPMODU CONNECTORS
EXACTLY AS SHOWN ON DWG 200.50D2



				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION TRANSFER CONTROL BOARD PART NO. 200.9			
				MACROMODULAR PROJECT		APPROVED BY <i>Cem</i> FOR MANUF. DATE <i>7/26/71</i>		ENG. REO	DRAWING NO. 200.9D1
CHANGE NO. <i>1</i>		DATE <i>3-27-72</i>				DESCRIPTION <i>E.C.O. 0258 NTK</i>		CHECKED <i>NTK</i>	

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M04	2	P1 P7
M08	1	P16
M10	8	P3 P4 P5 P6 P8 P10 P15 P17
M11	2	P11 P14
M16	1	P2
M30	1	P12
M31	2	P9 P13

CAPACITORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
10,000pf	3	C01 C02 C03

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R0	1	R18
R1	17	R01 R04 R06 R08 R10 R19 R21 R22 R24 R26 R29 R30 R31 R33 R36 R39 R40
R3	19	R02 R03 R05 R07 R09 R11 R12 R13 R16 R17 R20 R23 R25 R27 R28 R32 R34 R37 R38
R4	3	R14 R15 R35

CIRCUIT BOARD
PTV0125-0
ONE REQUIRED

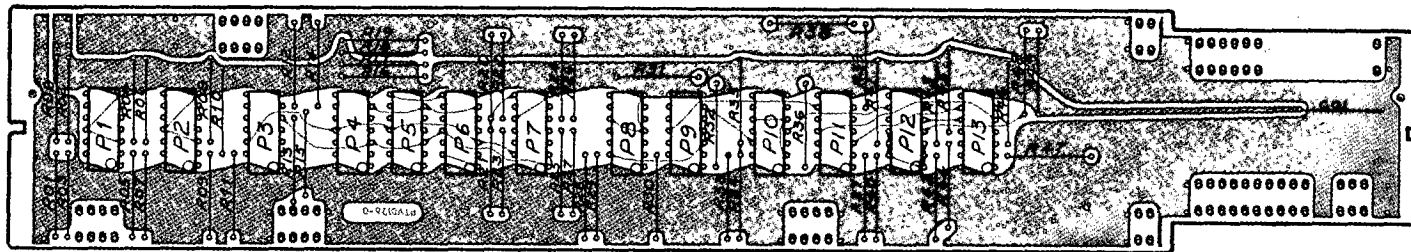
CONNECTORS
AMPMODU NO. 85863-4
FIFTY ONE REQUIRED

NOTE

R0 = JUMPERS ZERO OHM
R1 = 1.5K OHM 1% FILM RESISTOR
R3 = 121 OHMS 1% FILM RESISTOR
R4 = 15K OHMS 5% CARBON COMP.

CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST TRANSFER CONTROL BOARD PART NO. 200.9			
APPROVED		ENG. REO	DRAWING NO.
BY	FOR	DATE	200.9D2
		DRAWN BY	
		MBP	
		CHECKED	DATE
			7-15-71

NOTE: INSTALL FEMALE AMP MODU CONNECTORS
EXACTLY AS SHOWN ON DRAWING 200.50D2.



CHANGE NO.	DATE	DESCRIPTION	COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION MODE CONTROL BOARD PART NO. 200.13			
					APPROVED BY <i>Cern</i> FOR MANUF. DATE 7-28-71		ENG. REO	DRAWING NO. 200.13D1
			MACROMODULAR PROJECT		CHECKED NTK	DATE 7-27-71		

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M01	2	P8 P11
M01B	1	P9
M04B	3	P1 P2 P12
M10	4	P4 P5 P10 P13
M12	3	P3 P6 P7

CAPACITORS

TYPE	REQUIRED	LOCATION
CK-103	1	C01

RESISTORS

TYPE	REQUIRED	LOCATION
R0	2	R39 R48
R1	9	R00 R02 R04 R06 R35 R41 R43 R45 R46
R2	8	R08 R10 R12 R14 R20 R22 R24 R26
R3	24	R01 R03 R05 R07 R09 R11 R13 R15 R21 R23 R25 R27 R28 R29 R30 R32 R33 R34 R36 R37 R40 R42 R44 R47

RESISTORS

TYPE	REQUIRED	LOCATION
R4	7	R16 R17 R18 R19 R31 R38 R49

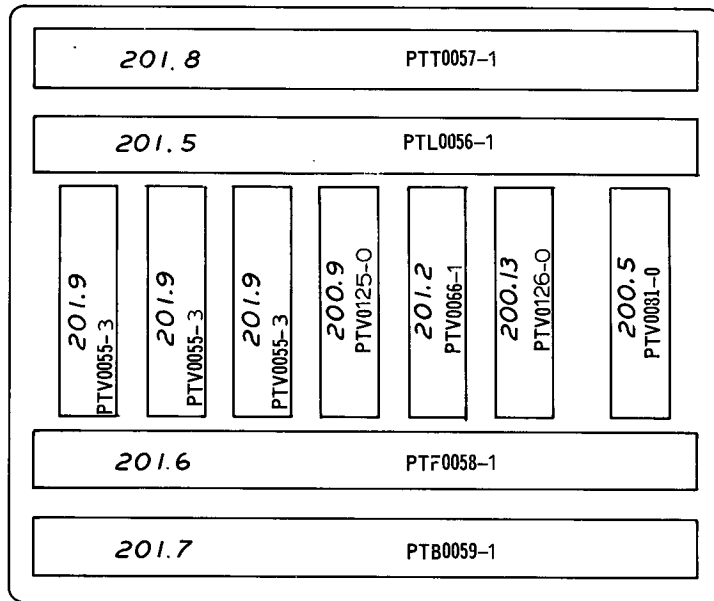
CIRCUIT BOARD
PTV 0126-G
ONE REQUIRED

CONNECTORS
AMPMODU NO. 85863-4
57 REQUIRED

R0 JUMPERS
R1 1.5K 1% FILM RESISTOR
R2 750 OHM 1% FILM RESISTOR
R3 121 OHM 1% FILM RESISTOR
R4 15K OHM 5% CARBON COMP.

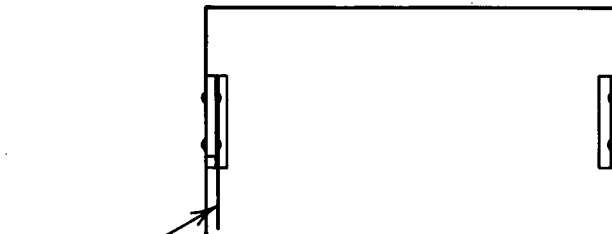
CHANGE NO.		DATE	DESCRIPTION
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>			
<p align="center">MACROMODULAR PROJECT</p>			
<p>TITLE PARTS LIST MODE CONTROL BOARD PART NO. 200.13</p>			
APPROVED		ENG. REO	DRAWING NO.
BY	FOR	DATE	
	MANUF.	7-28-71	200.13D2
CHECKED		DRAWN BY	DATE
		MAK	7-26-71

201.4



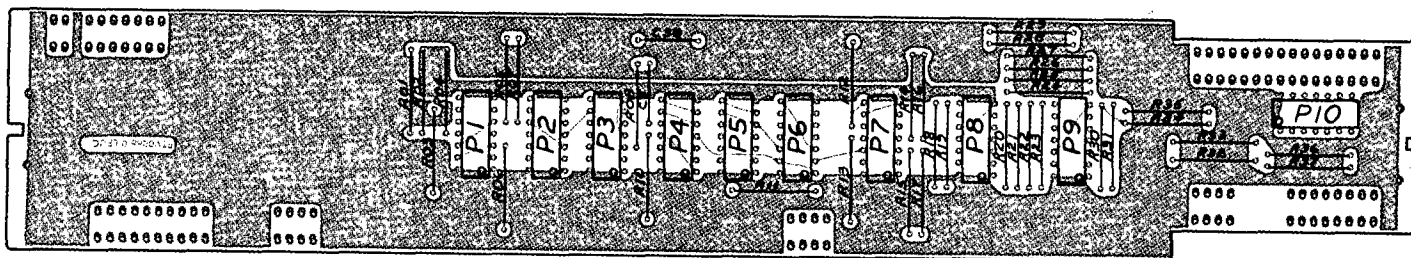
201.4

200.1
ONE CELL CASE
ASSEMBLY



KEY STOP
LEFT HAND SHROUD

4	11-16-73	E.C.O. 0301	MACROMODULAR PROJECT TITLE ASSEMBLY SCHEMATIC & PARTS LIST LOGIC FUNCTION UNIT PART NO. 201			
3	7-27-71	E.C.O. 0207				
2	10-6-70	E.C.O. 0050				
1	6-18-70	NO. CHANGE ON LMB.				
CHANGE NO.	DATE	DESCRIPTION	APPROVED BY: <i>NTK</i> FOR: <i>WAC</i> DATE: <i>11-16-73</i> DRAWN BY: <i>PLL</i> CHECKED: <i>NTK</i> DESIGNED: <i>WAC</i> 3-28-70			
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			DRAWING NO. 201.0D			



NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DWG. 200.5002.

		COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LOGIC FUNCTION UNIT LOCAL CONTROL BOARD PART NO. 201.2					
				MACROMODULAR PROJECT		APPROVED BY <i>CDM</i> FOR <i>MANUF.</i> DATE <i>3-20-70</i> BY <i>CDM</i> FOR <i>MANUF.</i> DATE <i>7-18-71</i>		ENG. <i>DLS</i> DRAWN BY <i>PLL</i> CHECKED <i>NTK</i>	DRAWING NO. <i>201.2D1</i> DATE <i>3-19-70</i>
CHANGE NO.	DATE	DESCRIPTION							
<i>1</i>	<i>10-6-70</i>	<i>E.C.O. 0050</i>							

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M06	2	P8 P9
M07	1	P2
M10	2	P3 P7
M11	3	P1 P4 P5
M20	1	P10
M30	1	P6

CAPACITORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
18 pf	2	C9 C38

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R0	2	R06 R11
R1	7	R02 R04 R12 R14 R16 R36 R37
R2	11	R05 R08 R10 R18 R19 R20 R21 R22 R23 R30 R31
R3	8	R01 R03 R07 R13 R15 R17 R32 R33
R5	8	R24 R25 R26 R27 R28 R29 R34 R35

CONNECTORS
AMPMODU NO. 85863-4
69 REQUIRED

CIRCUIT BOARD
PTV0066-1
ONE REQUIRED

NOTE:

R0 = JUMPER
R1 = 1.5 K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R5 = 57.6 OHM 1% FILM RESISTOR
18 pf DIPPED SILVER MICA 5%

1	7-27-71	E.C.O. 0307
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST LOGIC MODULE CONTROL BOARD PART NUMBER 201.2		
APPROVED		ENG. <i>DLS</i>
BY	FOR	DATE
<i>CRAN</i>	MANUF.	6/8/70
	MANUF.	7-28-71
CHECKED		DATE
<i>21.7K</i>		6/16/70

Test Procedure

Logic Module Control Board #201.2

This board contains one critical delay whose proper value must be checked on each board prior to assembly into a Logic Module.

The delay value must be longer than a specified minimum value. If the delay value is excessively large, the operation of the module will be needlessly slowed down. If the delay value is excessively short, the module may perform incorrectly under certain conditions.

Procedure

Tie pins L3 and T81 high and tie pins F2, F3, F4 and F5 to -5.2 volts. Apply a square wave signal with a period of 300 nanoseconds or greater to pin T31. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin T31 with channel one of a 454 oscilloscope. Observe the waveform at pin L2 with the second channel. The delay between the two waveforms, measured from mid-point of each transition, should be 56 nanoseconds or greater. The delay should be observed for both positive and negative going transitions, and both should be 56 nanoseconds or greater. If the smaller of the two delays is less than 56 ns, increase the value of C9 and C38. If the smaller of the two delays is greater than 65 nanoseconds the value of C9 and C38 should be reduced.

The final capacitor values and the measured delays for each board should be recorded on the test sheet provided for that board, along with the serial number of the board.

The circuit board should be carefully inspected to insure that the foregoing procedure has not resulted in damage to the circuit board, particularly in the areas where fresh soldering has taken place. All flux residues should be thoroughly removed.

CHG.	E.C.O.	DATE	APPR.
		12-28-70	
1	0301	11-16-73	MJS

201.2D4

LOGIC

METALCRAFT "AUTOGRAPH" OR EQUIVALENT:
 BLANK SIZE: 4" X 2" SHEARED WITH
 SQUARE CORNERS. BLACK LETTERS,
 VOGUE BOLD 12 POINT BOLD FACE TYPE
 CENTERED TOP, BOTTOM AND SIDES WITH 6
 POINT SPACING ON LIGHT BROWN PMS 466
 BACKING. MANUFACTURED FROM .016
 THICK ALUMINUM WITH SOLVENT ACTIVATED
 PERMANENT ADHESIVE BACKING.

NOTE: PANTONE MATCHING SYSTEM (PMS)

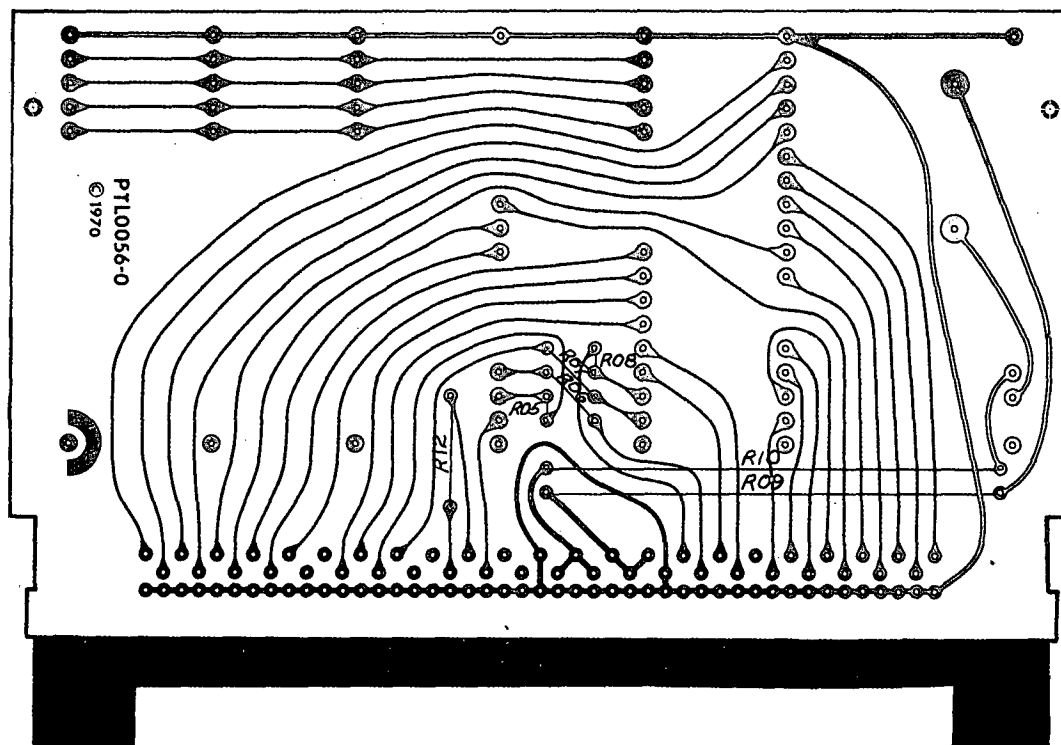
COMPUTER SYSTEMS LABORATORY
 WASHINGTON UNIVERSITY
 ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE

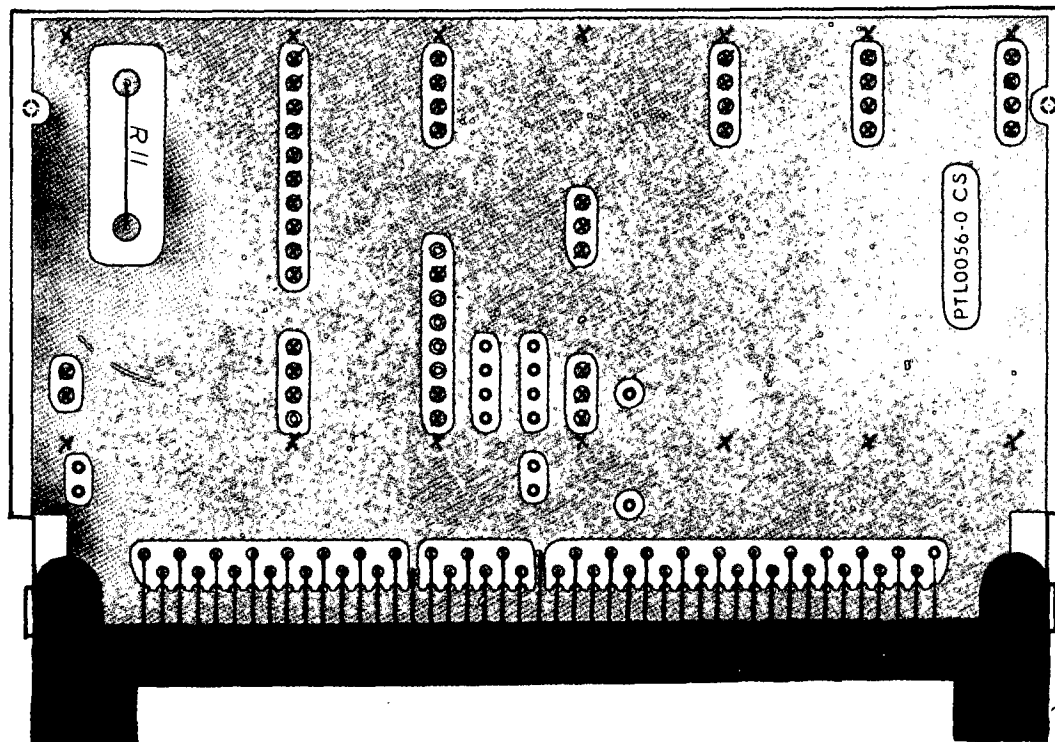
IDENTIFICATION LABEL
 LOGIC MODULE
 PART #201.4

APPROVED			ENG	DRAWING NO.
BY	FOR	DATE	NTK	
<i>Maur</i>	<i>Prod.</i>	<i>7/28/70</i>	DRAWN BY	201.4D
			KM	
			CHECKED	DATE
			<i>Maur</i>	6-16-70



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LATERAL MOTHERBOARD ASSEMBLY: SIGNAL SIDE PART NO. 201.5						
					APPROVED BY FOR DATE CEM MANUF. 3-28-71 CEM MANUF. 7-28-71		ENG. DLS DRAWN BY PLL CHECKED NTK		DRAWING NO. 201.5D1 DATE 3-27-70		
CHANGE NO.	DATE	DESCRIPTION	MACROMODULAR PROJECT								
3	10-6-70	E.C.O. 0050 CEM NTK									
2	6-18-70	CHANGE DRAWING NO. AND REMOVE NOTE									
1	6-11-70	ADDED 206 AND 207 AS PART NO. USED,									

NOTE: AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DRAWINGS 200.50D1 AND 200.50D2. (55 PINS)



NOTE: SEE DRAWING NUMBER 200.50D28 FOR CONNECTOR MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY		TITLE			
			WASHINGTON UNIVERSITY		COMPONENT IDENTIFICATION			
			ST. LOUIS, MISSOURI		LOGIC LATERAL MOTHERBOARD CONNECTOR ASSEMBLY			
					PART NO. 201.5			
					COMPONENT SIDE			
					APPROVED		ENG. DLS	DRAWING NO.
					BY	FOR	DATE	
					Cem	MANUF.	16 Oct 70	201.5D2
					Cem	MANUF.	7-28-71	
					CHECKED		DATE	
					NTK		10/14/70	
CHANGE NO.	DATE	DESCRIPTION						
2	11-24-71	E.C.O. 0232 MXP						
1	11-30-70	E.C.O. 0098 Clem						

COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY
ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE
COMPONENT IDENTIFICATION
LOGIC LATERAL MOTHERBOARD CONNECTOR ASSEMBLY
PART NO. 201.5
COMPONENT SIDE

APPROVED			ENG. DLS	DRAWING NO.
BY	FOR	DATE		
Cem	MANUF.	16 Oct 70	DRAWN BY	201.5D2
Cem	MANUF.	7-28-71	CHECKED	DATE
			NTK	10/14/70

JUMPERS
SIX REQUIRED
R05
R06
R07
R08
R09
R10

RESISTOR 280K OHM 1% FILM
R12

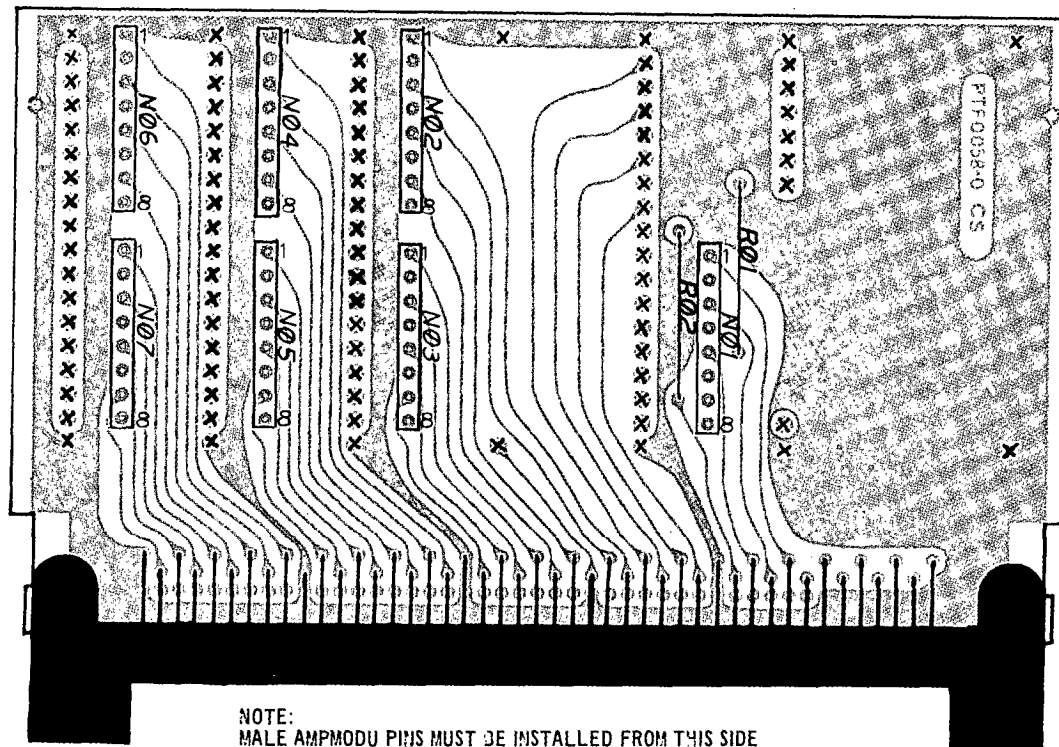
FUSE BUSSMAN GFA 3/4 AMP
ONE REQUIRED
R11

AMP CONNECTOR
583 464-1
ONE REQUIRED

CONNECTORS
AMP MODU NO. 85931-5
FIFTY-FOUR REQUIRED

CIRCUIT BOARD
PTL0056-1
ONE REQUIRED

5	11-24-71	E.C.O. 0232	<i>MBP</i>
4	7-27-71	E.C.O. 0207	<i>MBP</i>
3	12-11-70	E.C.O. 0127	<i>MBP</i>
2	10-6-70	E.C.O. 0050	<i>clm</i> <i>MBP</i>
1	6-18-70	CHANGED DRAWING NO.	<i>clm</i>
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY			
WASHINGTON UNIVERSITY			
ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST LOGIC LATERAL MOTHER BOARD PART NO. 201.5			
APPROVED			ENG. <i>clm</i>
BY <i>clm</i>	FOR MANUF.	DATE 6/8/70	DRAWN BY MBP
<i>clm</i>	MANUF	7-28-71	CHECKED <i>MBP</i>
			DATE 6/16/70



NOTE:
MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.
(85 PINS)

NOTE:
SEE DRAWING NUMBER 200.50D29 FOR
CONNECTOR MOUNTING ORIENTATION.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LOGIC FACEPLATE MOTHERBOARD CONNECTOR ASSEMBLY PART NO. 201.6			
				MACROMODULAR PROJECT		APPROVED BY <i>Cem</i> FOR <i>MANUF</i> DATE <i>20 Nov 70</i>		ENG. DLS	DRAWING NO. 201.6D1
						BY <i>Cem</i> MANUF. 7-28-71		DRAWN BY PLL	
								CHECKED NTK	DATE 11-17-70
CHANGE NO.	DATE 11-13-70	DESCRIPTION E.C.O. 0082 <i>Cem</i>							

AMP CONNECTOR
583 464-1
ONE REQUIRED

RESISTORS 15K OHM 5% 1/4WATT CARBON
TWO REQUIRED
R01
R02

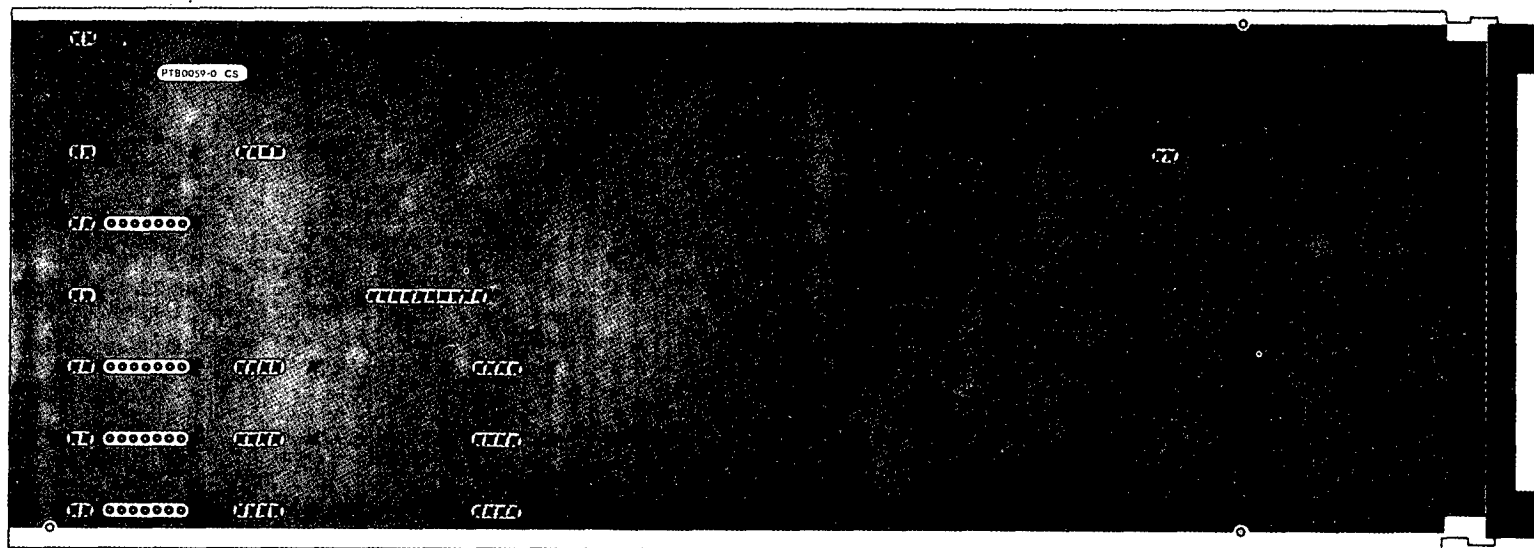
SPRAGUE NETWORK LTN-2
SEVEN REQUIRED
N01
N02
N03
N04
N05
N06
N07

CIRCUIT BOARD
PTF0058-1
ONE REQUIRED

CONNECTOR
AMPMODU NO. 85931-5
EIGHTY-FIVE REQUIRED

1		7-27-71		E.C.O. 0207	
CHANGE NO.		DATE		DESCRIPTION	
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>					
<p align="center">MACROMODULAR PROJECT</p>					
<p>TITLE PARTS LIST FACEPLATE MOTHERBOARD CONNECTOR ASSEMBLY PART NO. 201.6</p>					
APPROVED			ENG.	DRAWING NO.	
BY	FOR	DATE	DLS	201.6D2	
Cam	MANUF	10-14-70			
	MANUF	7-28-71	MBP		
			CHECKED	DATE	
			MTK	10/14/70	

10/21/70



NOTE: MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.
(88 PINS)

NOTE: SEE DRAWING NUMBER 200.50D27
FOR CONNECTOR MOUNTING
ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI	TITLE COMPONENT IDENTIFICATION BOTTOM MOTHERBOARD ASSEMBLY PART NO. 201.7			
CHANGE NO.	DATE	DESCRIPTION	MACROMODULAR PROJECT	APPROVED BY <i>COM</i> FOR <i>MANUF</i> DATE <i>10/14/70</i>		ENG. DLS DRAWN BY <i>PLL</i>	DRAWING NO. 201.7D1
				<i>COM</i> <i>MANUF.</i> <i>7-28-71</i>		CHECKED NTK	DATE 10/14/70

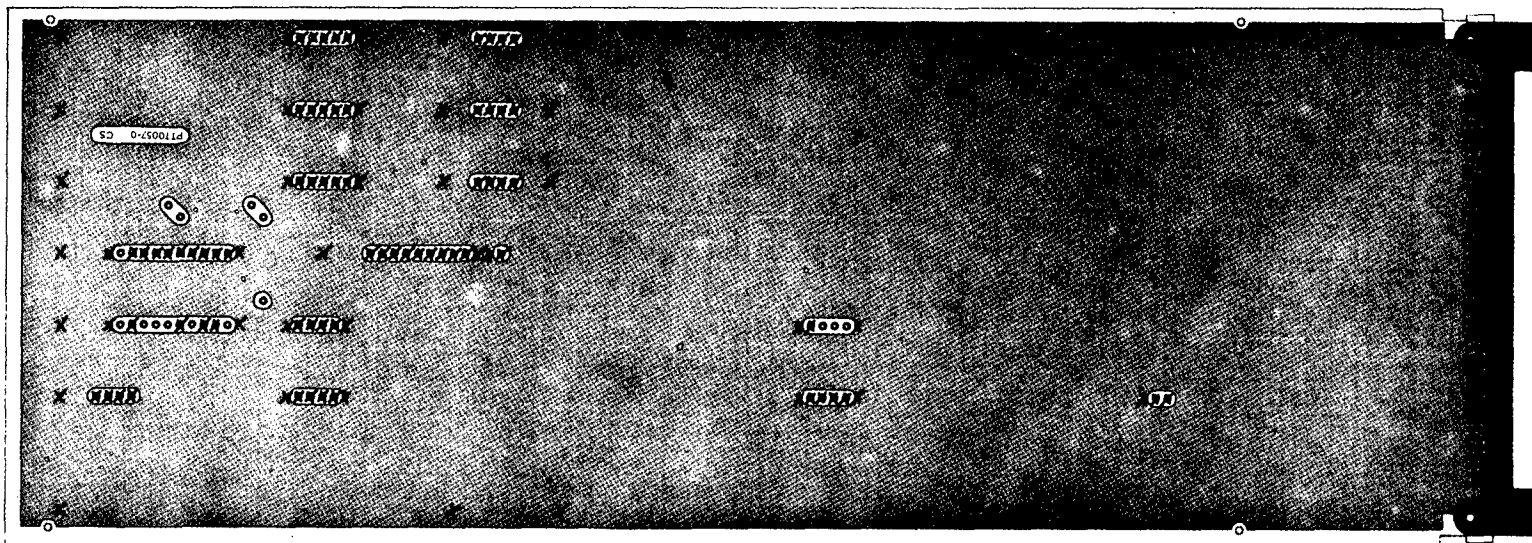
AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTORS
AMPMODU NO. 85931-5
EIGHTY EIGHT REQUIRED

CIRCUIT BOARD
PTB0059-1
ONE REQUIRED

1	7-27-71	E.C.O. 0207 <i>DATA</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST BOTTOM MOTHERBOARD ASSEMBLY PART NO. 201.7		
APPROVED		ENG. <i>DLS</i>
BY	FOR	DATE
<i>COM</i>	MANUF	<i>10/14/70</i>
<i>COM</i>	MANUF	7-28-71
DRAWN BY		DRAWING NO.
MBP		201.7D2
CHECKED		DATE
<i>DATA</i>		10/14/70

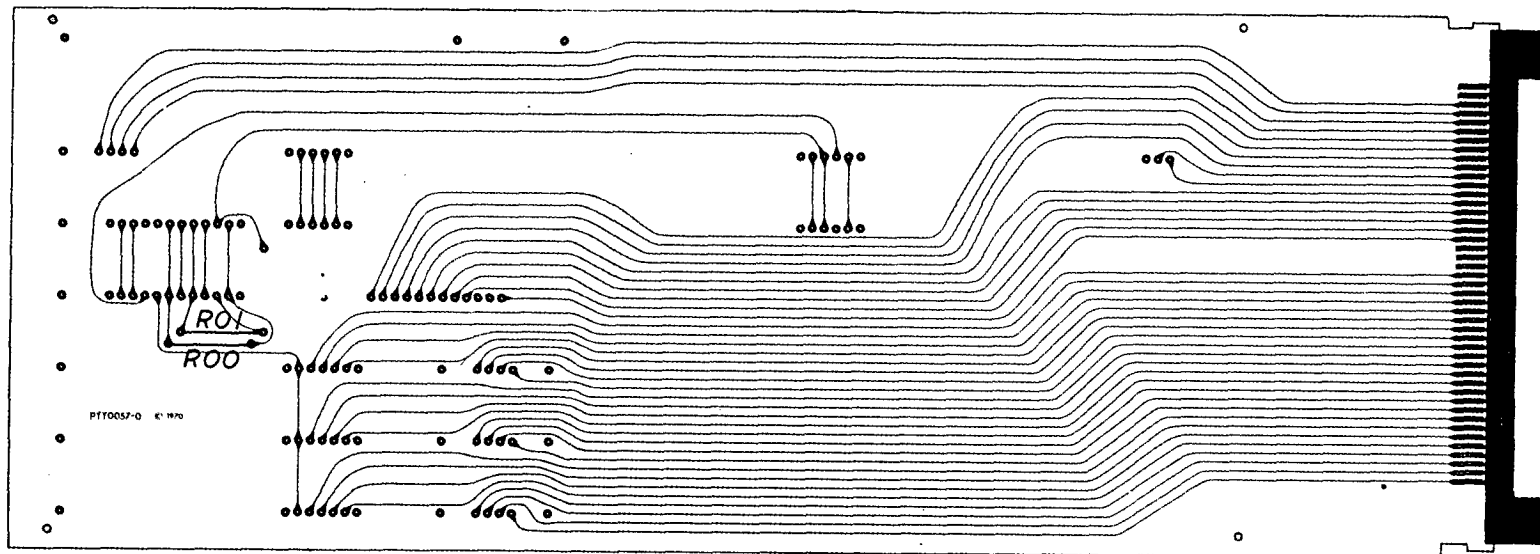
10/27/70



NOTE: SEE DRAWING NUMBER 200.50D26
FOR CONNECTOR MOUNTING
ORIENTATION.

NOTE: MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.
(106 PINS)

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY, ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LOGIC TOP MOTHERBOARD ASSEMBLY COMPONENT SIDE PART NO. 201.8				
						APPROVED BY FOR DATE CDM MANUF 10/28/70 CDM MANUF 7-28-71			ENG. DLS DRAWN BY FLL	DRAWING NO. 201.8D1
1		11-30-70		E.C.O. 0098				CHECKED NTK	DATE 10/14/70	
CHANGE NO.	DATE	DESCRIPTION								



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LOGIC TOP MOTHERBOARD ASSEMBLY SIGNAL SIDE PART NO. 201.8			
					APPROVED BY <i>CLM</i> FOR <i>MANUF</i> DATE <i>3 Apr 70</i>		ENG. <i>DLS</i> DRAWN BY <i>PLL</i>	DRAWING NO. 201.8D2
1	10/14/70	CHANGED PART NO., DWG. NO., TITLE <i>Com NTK</i>	MACROMODULAR PROJECT		<i>CLM</i> <i>CLM</i>	<i>MANUF</i> <i>MANUF</i>	<i>3 Apr 70</i> <i>7-28-71</i>	CHECKED <i>NTK</i> DATE <i>3-28-70</i>
CHANGE NO.	DATE	DESCRIPTION						

AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTORS
AMPMODU NO. 85931-5
ONE HUNDRED SIX REQUIRED

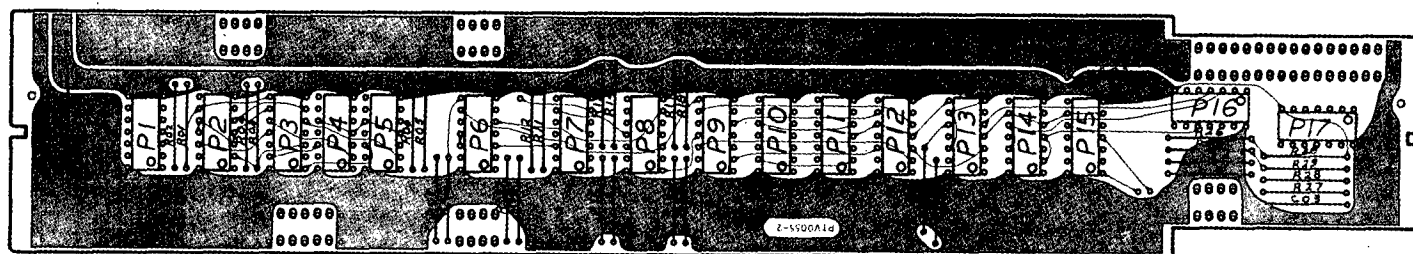
JUMPERS
TWO REQUIRED
R00
R01

CIRCUIT BOARD
PTT0057-1
ONE REQUIRED

2	7-27-71	E.C.O. 0207	
1	11-30-70	E.C.O. 0098	Cam
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST LOGIC TOP MOTHERBOARD ASSEMBLY PART NO. 201.8			
APPROVED		ENG.	DRAWING NO.
BY	FOR	DATE	201.8D3
Cam	MANUF	10/14/70	
CHECKED		DATE	
MBP		7-28-71	
NTR		10/14/70	

10/20/70

NOTE:
INSTALL FEMALE AMPMODU CONNECTORS
EXACTLY AS SHOWN ON DWG. 200.50D2.



CHANGE NO.	DATE	DESCRIPTION	COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LOGIC FUNCTION UNIT DATA BOARD PART NO. 201.9			
			MACROMODULAR PROJECT		APPROVED BY <i>Cem</i> FOR MANUF. DATE <i>25 Nov 70</i> BY <i>Cem</i> FOR MANUF. DATE <i>7-28-71</i>		ENG. <i>REO</i> DRAWN BY <i>DHO</i> CHECKED <i>NTK</i>	
							DATE <i>11-18-70</i>	

INTEGRATED CIRCUITS		
TYPE	REQUIRED	LOCATION
M01B	1	P1
M04	2	P6 P7
M10	6	P3 P9 P10 P13 P14 P15
M20	2	P16 P17
M31	3	P8 P11 P12
M16	2	P4 P5
M47	1	P2

CAPACITORS*		
TYPE	REQUIRED	LOCATION
10,000 pf	3	C01 C02 C03

*SPRAGUE TYPE CK-103
CERAMIC DISC 50WVDC

RESISTORS		
TYPE	REQUIRED	LOCATION
R0	2	R21 R22
R1	12	R05 R06 R11 R12 R13 R14 R17 R18 R27 R28 R29 R30
R3	16	R01 R02 R03 R04 R07 R08 R09 R10 R15 R16 R19 R20 R23 R24 R25 R26

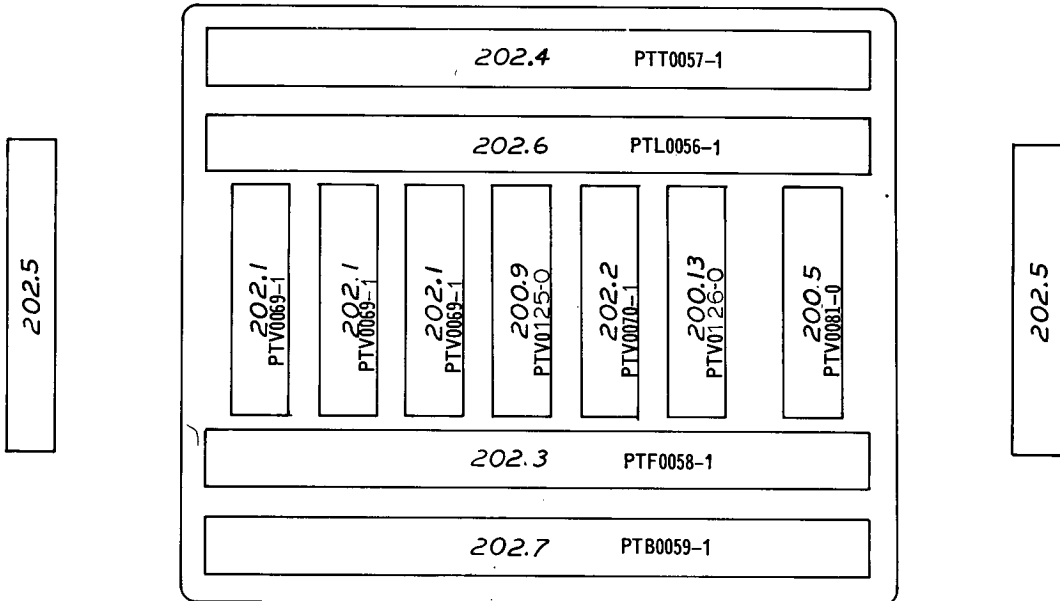
CONNECTORS
AMPMODU NO. 85863-4
54 REQUIRED

PRINTED CIRCUIT BOARD
PTV0055-3
ONE REQUIRED

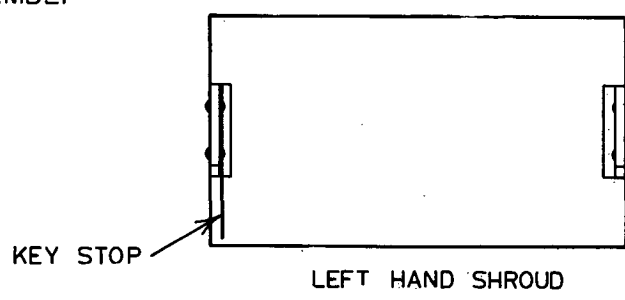
NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR

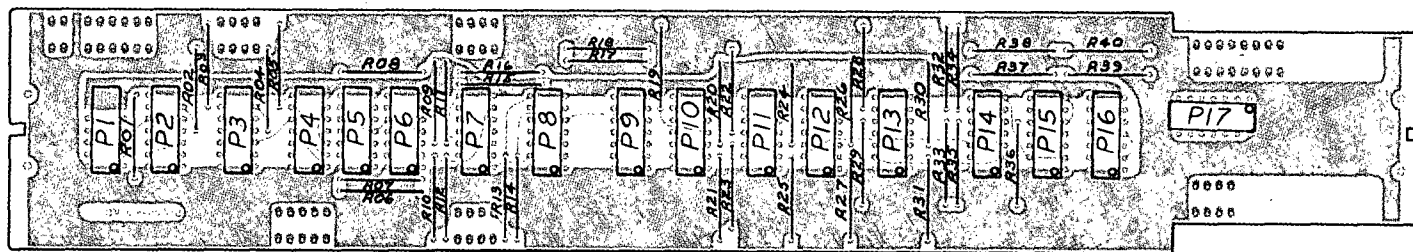
1	7-27-71	E.C.O. 0207	
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST LOGIC DATA BOARD PART NO. 201.9			
APPROVED			ENG. REO
BY	FOR	DATE	DRAWING NO.
CEAN	MANUF.	25-MAY-71	201.9D2
CEAN	MANUF.	7-28-71	CHECKED
			DATE
		NTK	11-18-70



200.1
ONE CELL CASE
ASSEMBLY



4	8-17-71	E.C.O. 0223	MACROMODULAR PROJECT. TITLE ASSEMBLY SCHEMATIC & PARTS LIST ADDITION UNIT PART NO. 202			
3	7-27-71	E.C.O. 0208				
2	10-6-70	E.C.O. 0045				
1	6-18-70	NO. CHANGE ON LMB.				
CHANGE NO.	DATE	DESCRIPTION	APPROVED BY FOR DATE NTK <i>Drum</i> 11-20-70 NAME DATE NAME DATE			
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			ENG WAC DRAWN BY PLL CHECKED NTK		DRAWING NO. 202.0D DATE 3-28-70	



INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DRAWING 200.50D2

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION ADDITION DATA BOARD PART NO. 202.1				
				MACROMODULAR PROJECT		APPROVED BY FOR DATE Cam MANUF. 3-20-70 Cam MANUF. 18 AUG 71			ENG. DLS	DRAWING NO. 202.101
						CHECKED NTK			DATE 3-19-70	
CHANGE NO.	DATE	DESCRIPTION								
1	10-6-70	E.C.O. 0045 Cam 7176								

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M01	1	P1
M04	2	P7 P8
M10	4	P2 P3 P4 P15
M12	1	P16
M16	2	P5 P6
M19	4	P10 P11 P12 P13
M20	1	P17
M30	1	P9
M31	1	P14

RESISTORS

TYPE	REQUIRED	LOCATION
R0	7	R08 R15 R16 R19 R22 R23 R36
R1	14	R07 R09 R11 R17 R18 R20 R24 R26 R29 R30 R33 R35 R39 R40
R3	19	R01 R02 R03 R04 R05 R06 R10 R12 R13 R14 R21 R25 R27

RESISTORS (cont)

TYPE	REQUIRED	LOCATION
		R28 R31 R32 R34 R37 R38

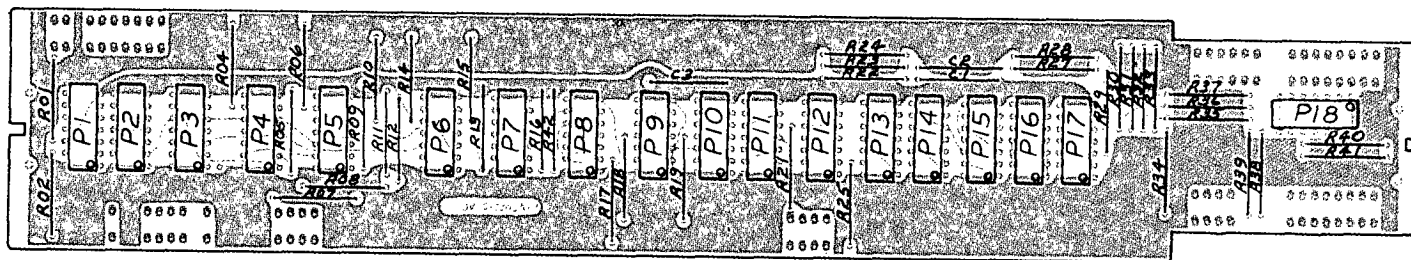
CONNECTORS
AMPMODU NO. 85863-4
52 REQUIRED

CIRCUIT BOARD
PTV0069-1
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR

3	8-17-71	E.C.O. 0223
2	1-14-71	E.C.O. 0147
1	10-6-70	E.C.O. 0045
CHANGE NO.	DATE	DESCRIPTION
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>		
<p align="center">MACROMODULAR PROJECT</p>		
<p>TITLE PARTS LIST ADDITION DATA BOARD PART NO. 202.1</p>		
APPROVED		ENG.
BY	FOR	DATE
MBP	MANUF.	4/4/70
CHECKED	DATE	174471
DATE	DATE	18A101
DRAWN BY MBP		DRAWING NO. 202.1D2
CHECKED		DATE 6/24/70



INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DRAWING 200.50D2

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION ADDITION LOCAL CONTROL BOARD PART NO. 202.2						
				MACROMODULAR PROJECT		APPROVED BY <i>Cem</i> FOR <i>MANUF.</i> DATE <i>3-20-70</i>			ENG. <i>DLS</i>	DRAWING NO. 202.2D1		
1		10-6-70	E.C.O. 0045			<i>Cem</i>	<i>NTK</i>	CHECKED <i>Cem</i> MANUF. DATE <i>17A.C.71</i>			DRAWN BY <i>PLL</i>	DATE 3-19-70
CHANGE NO.	DATE	DESCRIPTION										

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M01	2	P3 P16
M04	4	P4 P6 P9 P17
M07	1	P8
M08	1	P2
M10	5	P1 P7 P10 P11 P12
M11	2	P13 P14
M20	1	P18
M30	2	P5 P15

CAPACITORS *

TYPE	REQUIRED	LOCATION
130 pf	2	C1 C2
820 pf	1	C3

* DIPPED SILVER MICA 5% - 50 WVDC

RESISTORS

TYPE	REQUIRED	LOCATION
R0	9	R07 R08 R09 R21 R34 R38 R39 R40 R41
R1	12	R01 R05 R11 R12 R13 R16 R22 R29 R30 R31 R32 R42
R2	2	R23 R24
R3	10	R02 R04 R06 R10 R14 R15 R17 R18 R19 R25

RESISTORS (cont)

TYPE	REQUIRED	LOCATION
R5	6	R27 R28 R33 R35 R36 R37

CONNECTORS
AMPMODU NO. 85863-4
63 REQUIRED

CIRCUIT BOARD
PTV0070-1
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R5 = 57.6 OHM 1% FILM RESISTOR

3	11-16-73	E.C.O. 0301	4/13
2	8-17-71	E.C.O. 0223	
1	10-6-70	E.C.O. 0045	CH. 10/10/70
CHANGE NO.	DATE	DESCRIPTION	
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>			
<p align="center">MACROMODULAR PROJECT</p>			
<p>TITLE PARTS LIST ADDITION CONTROL BOARD PART NO. 202 2</p>			
APPROVED		ENG	DRAWING NO.
BY	FOR	DATE	
Cam	MANUF.	4/7/70	DRAWN BY MBP
Cam	MANUF.	12/2/71	CHECKED
Cam			DATE 6/24/70

Test Procedure

Addition Module Control Board #202.2

This board contains two critical delays whose proper value must be checked on each board prior to assembly into an Addition Module.

Both delay values must be longer than a specified minimum value. If the delay value is excessively large, the operation of the module will be needlessly slowed down. If the delay value is excessively short, the module may perform incorrectly under certain conditions.

Procedure

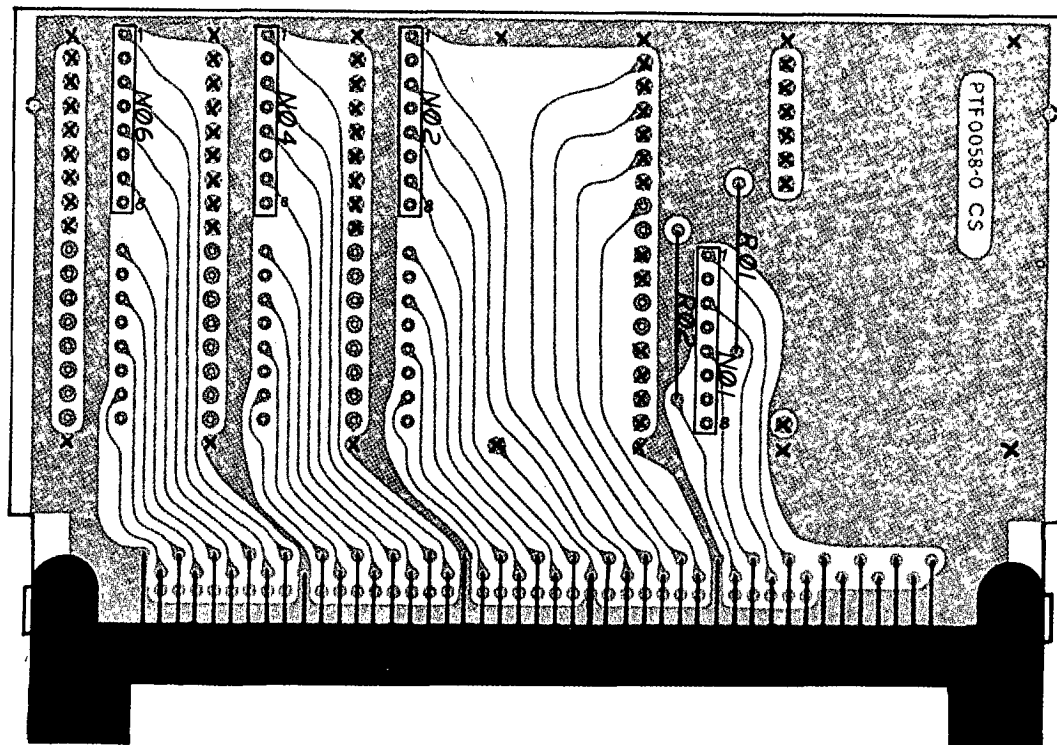
Test 1: Tie pins L3 and T88 high and tie pins L5 and T30 low. Apply a square wave signal with a period of 300 nanoseconds or greater to pin T31. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin T31 with channel one of a 454 oscilloscope. Observe the waveform at pin T82 with the second channel. The delay between the two waveforms, measured from mid-point of each transition, should be 157 nanoseconds or greater. The delay should be observed for both positive and negative going transitions, and both should be 157 nanoseconds or greater. If the smaller of the two delays is less than 157 nanoseconds, the value of capacitors C1 and C2 should be increased. If the value of the smaller of the delays is greater than 175 nanoseconds, the value of C1 and C2 should be reduced.

Test 2: Tie pin T31 high and pins L5, T30 and T88 low. Apply a square wave signal with a period of 300 nanoseconds or greater to pin L3. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin L3 with channel one of a 454 oscilloscope and observe the waveform at pin T82 with the second channel. The delay between the two waveforms, measured from mid-point of each transition, should be 133 nanoseconds or greater for the positive going output. There is no delay specification for a negative going output, but the delay will be much less than the positive transition delay. If the positive going delay is less than 133 nanoseconds, increase the value of C3 to increase the delay. If the positive going delay is greater than 150 nanoseconds, reduce the value of C3.

The final capacitor values and the measured delays for each board should be recorded on the test sheet provided for that board, along with the serial number of the board.

CHG.	E.C.O.	DATE	APPR.
		12-10-70	
1	0301	11-16-73	<i>4/15</i>

The circuit board should be carefully inspected to insure that the foregoing procedure has not resulted in damage to the circuit board, particularly in the areas where fresh soldering has taken place. All flux residues should be thoroughly removed.



NOTE:1
MALE AMPMODU PINS MUST BE
INSTALLED FROM THIS SIDE IN
LOCATIONS MARKED X PRECISELY
AS SHOWN IN DWG. 200.50D1 AND
200.50D2.

NOTE:2
SEE DRAWING NUMBER 200.50D29 FOR
CONNECTOR MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		COMPONENT IDENTIFICATION ADDITION FACEPLATE MOTHERBOARD ASSEMBLY PART NO. 203.3				
			MACROMODULAR PROJECT		APPROVED BY FOR DATE			ENG. DLS	DRAWING NO. 202.3D1
					CEM MANUF. 20 Nov 70			DRAWN BY PLL	
					CEM MANUF 18 AUG 71			CHECKED NTK	DATE 11-17-70
CHANGE NO.	DATE	DESCRIPTION							
2	11-13-70	E.C.O. 0082 <i>cem</i>							

RESISTORS 15K OHM 1/4WATT CARBON
TWO REQUIRED

R01
R02

SPRAGUE NETWORK LTN-2
FOUR REQUIRED

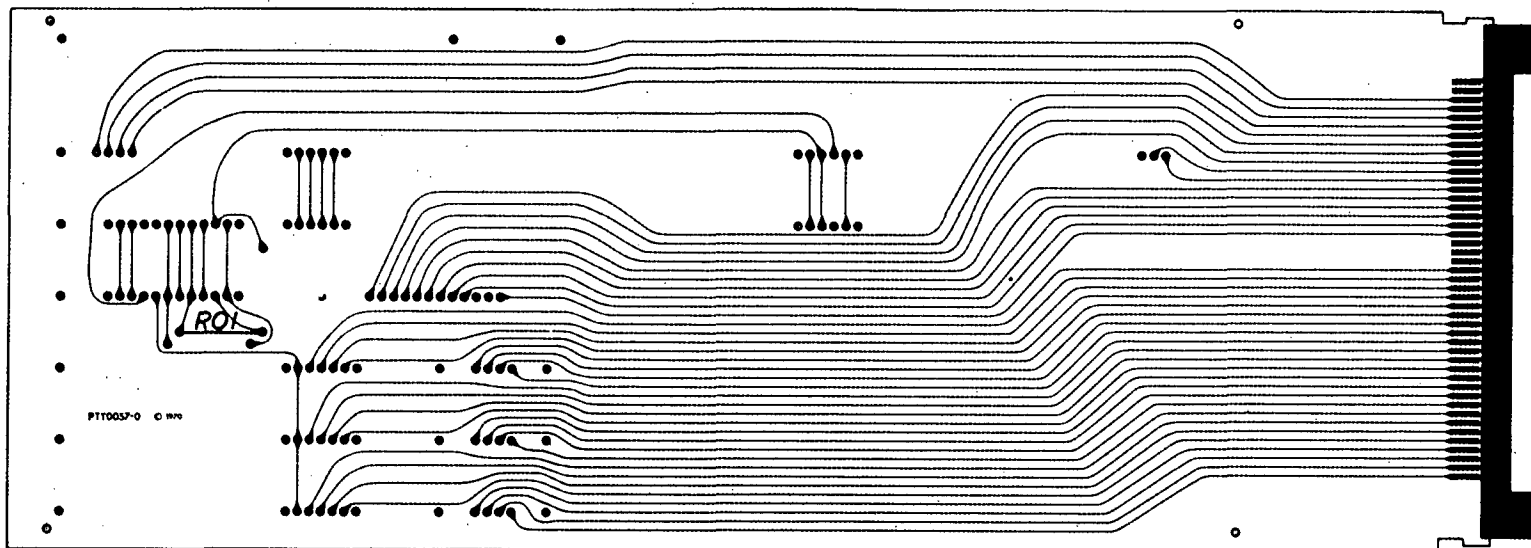
N01
N02
N04
N06

AMP CONNECTOR
583 464-1
ONE REQUIRED

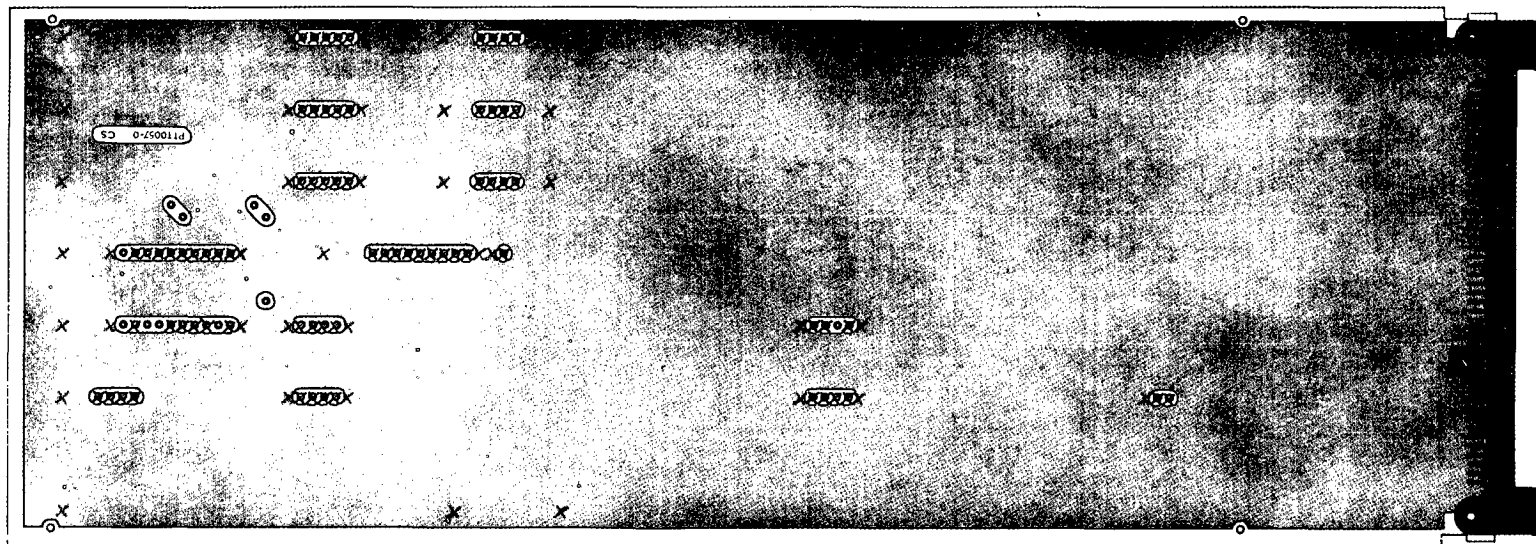
CONNECTOR
AMPMODU NO. 85931-5
FIFTY SEVEN REQUIRED

CIRCUIT BOARD
PTF0058-1
ONE REQUIRED

2	7-27-71	E.C.O. 0208
1	10-6-70	E.C.O. 0045
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST ADDITION FACEPLATE MOTHER BOARD PART NO. 202.3		
APPROVED		ENG.
BY	FOR	DATE
	MANUF.	4/7/70
DRAWN BY		DRAWING NO.
MBP		202.3D2
CHECKED		DATE
		6/24/70



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION ADDITION UNIT TOP MOTHER BOARD SIGNAL SIDE PART NO. 202.4			
					APPROVED BY FOR DATE CMM MANUF 3-28-70 CMM MANUF 18 AUG 71			
1 10/6/70 E.C.O. 0045 <i>TK</i>			MACROMODULAR PROJECT		ENG. DLS DRAWN BY PLL		DRAWING NO. 202.4D1	
CHANGE NO. DATE DESCRIPTION					CHECKED NTK		DATE 3-28-70	



NOTE:
MALE AMP MODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.

200.50D1 AND 200.50D2

NOTE: SEE DRAWING NUMBER
200.50D26 FOR CONNECTOR
MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION TOP MOTHERBOARD ASSEMBLY COMPONENT SIDE PART NO. 202.4			
					APPROVED BY FOR DATE CDM MANUF BAP TO CDM MANUF 18AUG 71			
3 11-30-70 E.C.O. 0070 CDM 2 0/10/70 E.C.O. 0044 CDM NTK 1 6/11/70 ADDED 207 AS PART NO. USED.			MACROMODULAR PROJECT		ENG. DLS DRAWN BY PLL		DRAWING NO. 202.4D2	
CHANGE NO. DATE DESCRIPTION					CHECKED NTK DATE 3-28-70			

JUMPERS
ONE REQUIRED
R01

AMP CONNECTOR
1-202 845-5
ONE REQUIRED

CONNECTORS
AMPMODU NO. 85931-5
ONE HUNDRED TEN REQUIRED

CIRCUIT BOARD
PTT0057-1
ONE REQUIRED

2	7-27-71	E.C.O. 0208
1	10-6-70	E.C.O. 0045
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST ADDITION UNIT TOP MOTHER BOARD PART NO. 202.4		
APPROVED		ENG.
BY	FOR	DATE
	MANUF.	4/10/70
DRAWN BY		DRAWING NO.
MBP		202.4D3
CHECKED	DATE	
<i>MTL</i>	6/24/70	

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METALCRAFT "AUTOGRAPH" OR EQUIVALENT;
 BLANK SIZE: $\frac{1}{4}$ " X 2" SHEARED WITH
 SQUARE CORNERS. BLACK LETTERS, VOGUE
 BOLD 12 POINT BOLD FACE TYPE CENTERED
 TOP, BOTTOM AND SIDES WITH 6 POINT
 SPACING ON GREY GREEN PMS 557 BACKING.
 MANUFACTURED FROM .016 THICK ALUMINUM
 WITH SOLVENT ACTIVATED PERMANENT
 ADHESIVE BACKING.

NOTE: PANTONE MATCHING SYSTEM (PMS)

COMPUTER SYSTEMS LABORATORY

WASHINGTON UNIVERSITY

ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE

IDENTIFICATION LABEL
 ADDITION MODULE
 PART #202.5

APPROVED

ENG

DRAWING NO.

BY

FOR

DATE

NTK

Maur

Prod.

7/28/70

DRAWN BY
KM

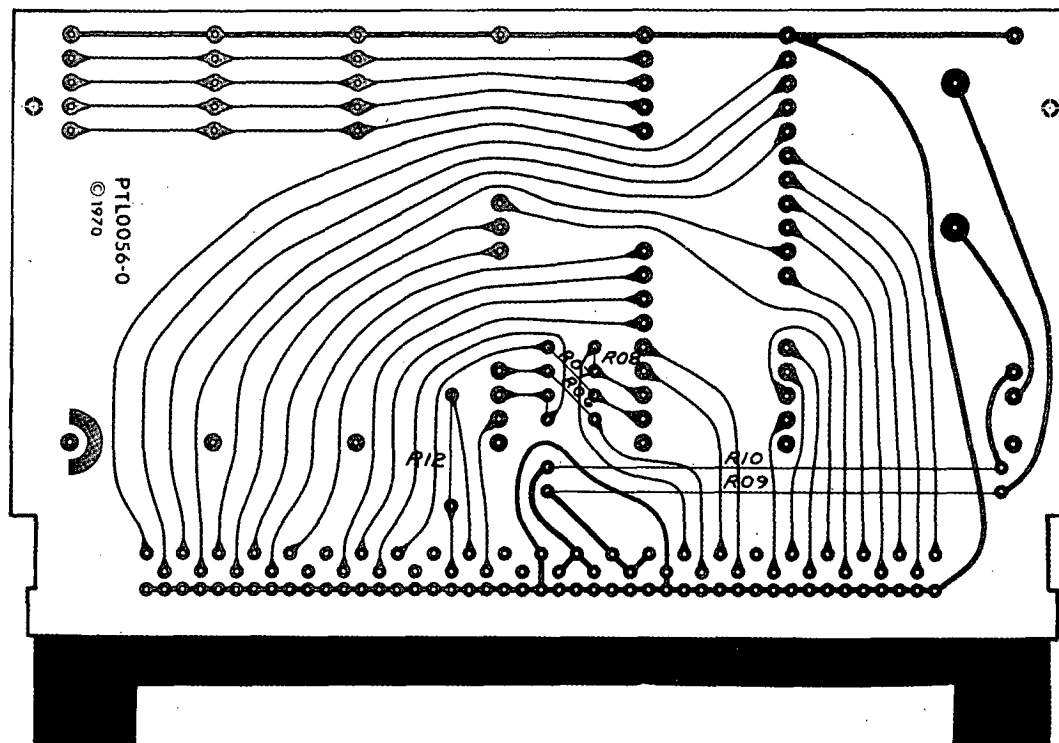
202.5D

CHECKED

DATE

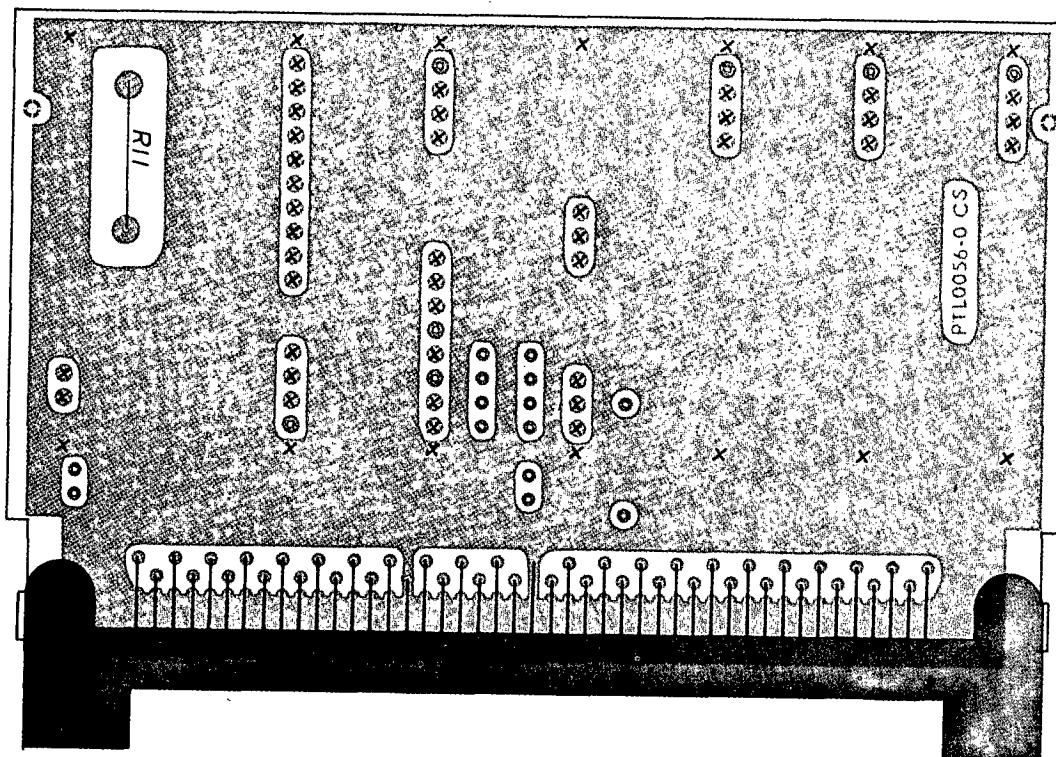
Maur

6-16-70



				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION LATERAL MOTHERBOARD ASSEMBLY SIGNAL SIDE PART NO. 202.6			
				MACROMODULAR PROJECT		APPROVED BY FOR DATE CDM MANUF 10/13/70 CDM MANUF 18 AUG 71		ENG. DLS DRAWN BY PLL	DRAWING NO. 202.6D1
CHANGE NO. 1		DATE 10/6/70				DESCRIPTION E.C.O. 0045 <i>Cem</i>		CHECKED NTK	

AMP MODU PINS MUST BE INSTALLED
FROM THIS SIDE IN LOCATIONS MARKED
X PRECISELY AS SHOWN IN DRAWINGS
200.50D1 AND 200.50L2



NOTE: SEE DRAWING NUMBER
200.50D28 FOR CONNECTOR
MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION ADDITION LATERAL MOTHERBOARD ASSEMBLY PART NO: 202.6 COMPONENT SIDE					
4	11-24-71	E.C.O. 0232			MLP	APPROVED BY FOR DATE Cam MANUF 3APR-72 Cam MANUF 18AUG-71			ENG. DLS	DRAWING NO. 202.6b2
3	11-30-70	E.C.O. 0094			Cem				DRAWN BY PLL	
2	9/10/70	E.C.O. 0044			Cem				CHECKED NTK	
1	6/19/70	ADD PART NOS. TO LIST			DATE 3-26-70					
CHANGE NO.	DATE	DESCRIPTION			MACROMODULAR PROJECT					

JUMPERS
SIX REQUIRED
R05
R06
R07
R08
R09
R10

RESISTOR 25.5K OHM 1% FILM
R12

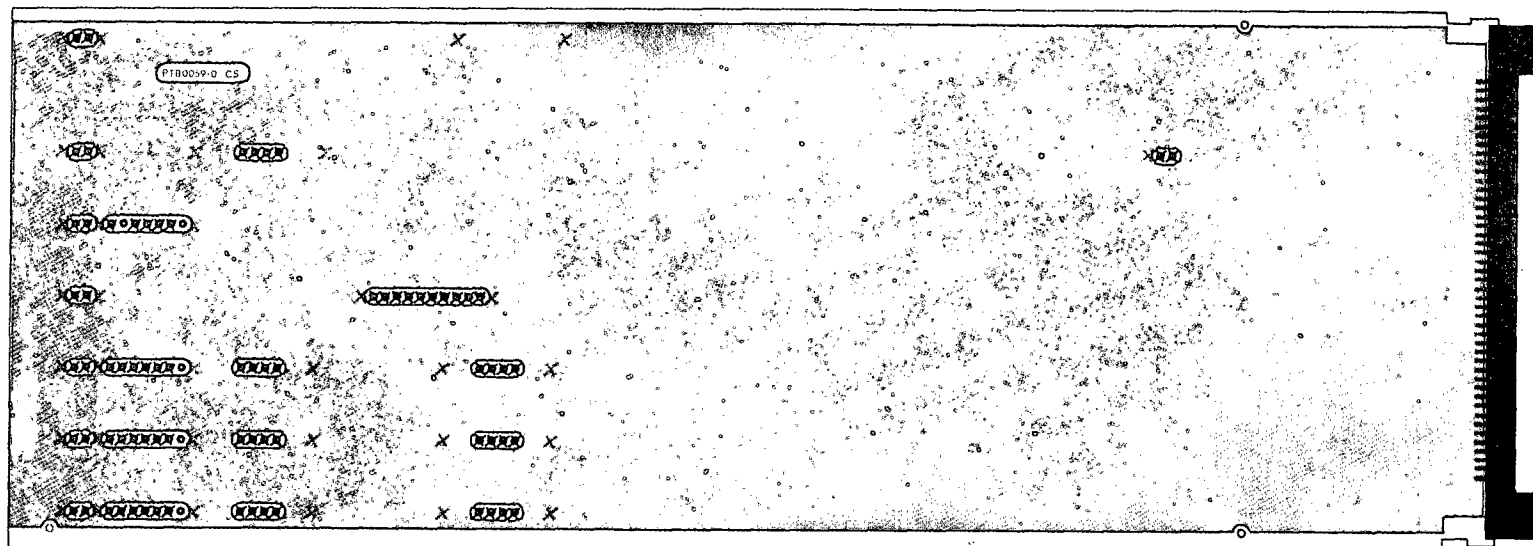
AMP CONNECTOR
583 464-1
ONE REQUIRED

CONNECTORS
AMPMODU 85931-5
FIFTY THREE REQUIRED

FUSE BUSSMAN GFA 3/4 AMP
ONE REQUIRED
R11

CIRCUIT BOARD
PTL0056-1
ONE REQUIRED

3	11-24-71	E.C.O. 0232 <i>MLP</i>
2	7-27-71	E.C.O. 0208
1	10-6-70	E.C.O. 0045 <i>NTK</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST LATERAL MOTHER BOARD ASSY PART NO. 202.6		
APPROVED		ENG. <i>CEM</i>
BY <i>cam</i>	FOR MANUF.	DATE 6/8/70
DRAWN BY		DATE
CHECKED <i>MBP</i>		DATE 6/18/70



NOTE:
MALE AMP MODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2

NOTE: SEE DRAWING NUMBER
200.50D27 FOR CONNECTOR
MOUNTING ORIENTATION.

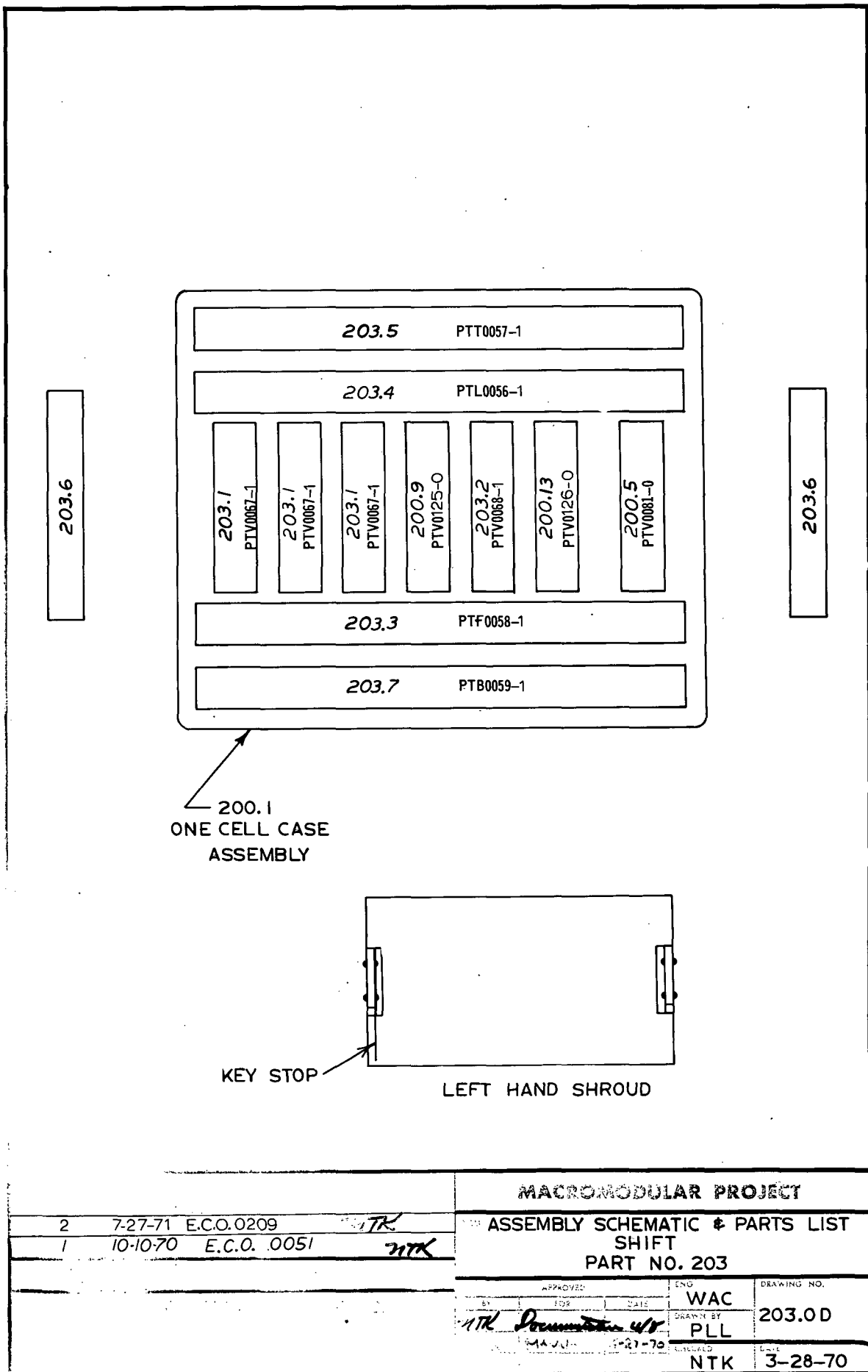
		COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION BOTTOM MOTHERBOARD ASSEMBLY PART NO. 202.7			
				APPROVED BY FOR DATE CEM MANUF 3 Apr 70 CEM MANUF 18 AUG 70			
2	0/10/70	E.C.O. 0044	MACROMODULAR PROJECT	ENG. DLS DRAWN BY PLL		DRAWING NO. 202.7b1	
1	6/11/70	ADDED 207 AS PART NO. USED.		CHECKED NTK		DATE 3-27-70	
CHANGE NO.	DATE	DESCRIPTION					

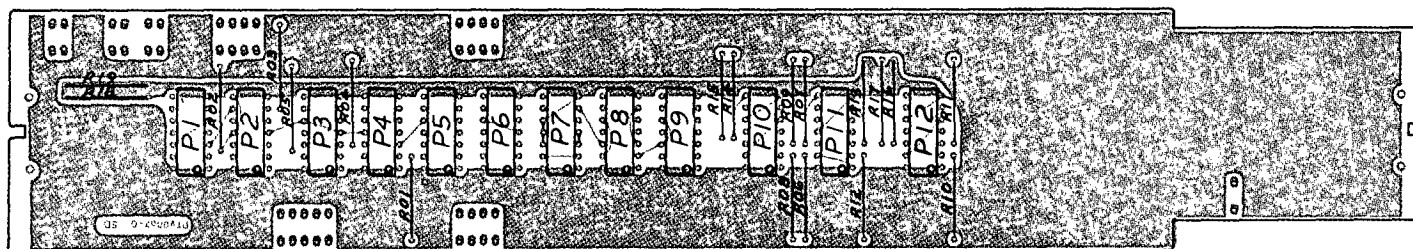
AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTORS
AMPMODU NO 85931-5
ONE HUNDRED ELEVEN REQUIRED

CIRCUIT BOARD
PTB0059-1
ONE REQUIRED

2	7-27-71	E.C.O. 0208
1	10-6-70	E.C.O. 0044
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST BOTTOM MOTHER BOARD ASSEMBLY PART NO. 200.11		
APPROVED		ENG.
BY	FOR	DATE
Cem	MANUF.	6/8/70
DRAWN BY		DRAWING NO.
MBP		202.7D2
CHECKED		DATE
JTK		6/16/70





NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DWG. 200.50D2.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		COMPONENT IDENTIFICATION SHIFT DATA BOARD PART NO. 203.1				
			MACROMODULAR PROJECT		APPROVED BY FOR DATE <i>Clem</i> MANUF. 3-20-70 <i>Clem</i> MANUF. 7-28-71			ENG. <i>DLS</i> DRAWN BY <i>PLL</i> CHECKED <i>NTK</i>	DRAWING NO. 203.1D1
CHANGE NO.	DATE	DESCRIPTION			DATE 3-19-70				
1	10-10-70	E.C.O. 0051 <i>Clem</i> <i>NTK</i>							

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M01B	1	P4
M04 B	4	P7 P10 P11 P12
M10	5	P1 P2 P3 P8 P9
M16	2	P5 P6

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R0	3	R01 R18 R19
R1	6	R07 R09 R11 R13 R14 R16
R3	10	R02 R03 R04 R05 R06 R08 R10 R12 R15 R17

CONNECTORS
AMPMODU NO. 85863-4
39 REQUIRED

CIRCUIT BOARD
PTV0067-1
ONE REQUIRED

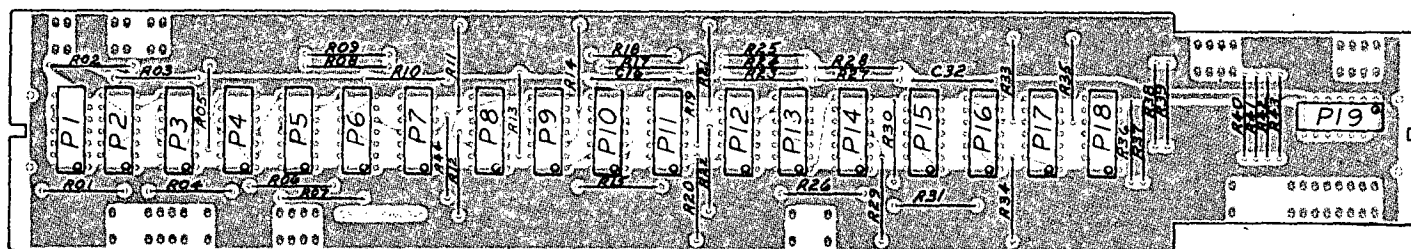
NOTE:

NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR

2	7-27-71	E.C.O. 0209	WTK
1	7-9-71	E.C.O. 0201	WTK
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST SHIFT DATA BOARD PART NO. 203.1			
APPROVED		ENG. DLS	DRAWING NO.
BY	FOR	DATE	203.1D2
Cam	MANUF.	8/7/70	
Cam	MANUF.	7-28-71	CHECKED
			DATE 6/24/70

270ct70



NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DWG. 200.50D2.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION SHIFT LOCAL CONTROL BOARD PART NO. 203.2				
			MACROMODULAR PROJECT		APPROVED BY FOR DATE Cern MANUF. 3-20-70 Cern MANUF. 7-28-71			ENG. DLS DRAWN BY PLL	DRAWING NO. 203.2DI
1 10-10-70 E.C.O. 0051 <i>NTK Cern</i>					CHECKED NTK			DATE 3-19-70	
CHANGE NO.	DATE	DESCRIPTION							

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M04	2	P5 P6
M05	1	P12
M06	1	P18
M10	8	P1 P7 P8 P9 P10 P11 P13 P14
M11	4	P3 P4 P15 P16
M20	1	P19
M30	1	P17
M31	1	P2

CAPACITORS*

TYPE	REQUIRED	LOCATION
33 pf	1	C32

*DIPPED SILVER MICA 5% 50WVDC

RESISTORS

TYPE	REQUIRED	LOCATION
R0	12	R04 R06 R07 R11 R13 R22 R26 R29 R33 R34 R35 R44
R1	8	R02 R10 R18 R19 R23 R24 R28 R30
R2	6	R17 R27 R36 R37 R42 R43
R3	12	R01 R03 R05 R08 R09 R12 R14 R15 R20

RESISTORS (cont)

TYPE	REQUIRED	LOCATION
R5	4	R21 R25 R31 R38 R39 R40 R41

CONNECTORS
AMPMODU NO. 85863-4
49 REQUIRED

CIRCUIT BOARD
PTV0068-1
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R5 = 57.6 OHM 1% FILM RESISTOR

3	7-27-71	E.C.O. 0209	NTK
2	2-12-71	E.C.O. 0157	NTK
1	10/10/70	E.C.O. 0051	NTK
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY			
WASHINGTON UNIVERSITY			
ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST SHIFT CONTROL BOARD PART NO. 203.2			
APPROVED		ENG. <i>RED</i>	DRAWING NO.
BY <i>CON</i>	FOR MANUF.	DATE 4/7/70	203.2D2
CHECKED <i>CON</i>	MANUF.	DATE 7-28-71	
		CHECKED <i>NTK</i>	DATE 6/24/70

Test Procedure

Shifter Control Board Part #203.2

This board contains two critical delays whose proper value must be checked on each board prior to assembly into a Shifter Module.

Both delay values must be longer than a specified minimum value. If the delay value is excessively large, the operation of the module will be needlessly slowed down. If the delay value is excessively short, the module may perform incorrectly under certain conditions.

Procedure

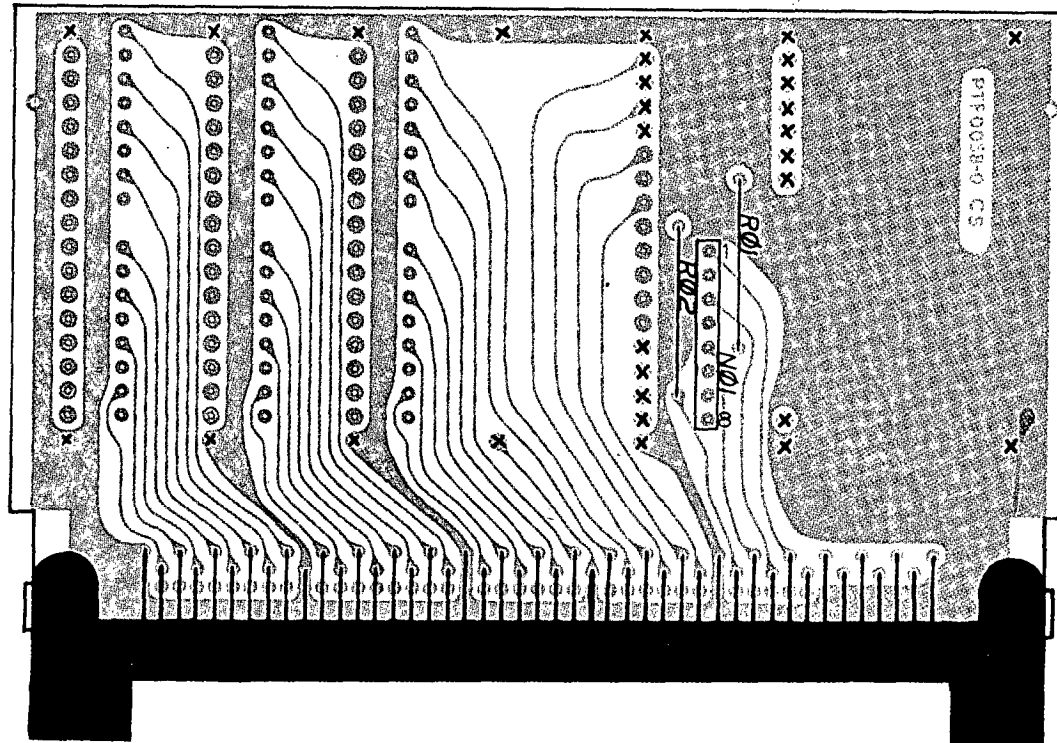
Test 1: Tie pin T83 low and apply one phase of a 200 nanosecond square wave to pin L3 and apply the opposite phase of the square wave to pin T31. Observe the waveform at pin L2 with the second channel of a 454 oscilloscope. Observe the waveform at pin L3 with the first channel. The delay between the two waveforms, measured from mid-point of each transition, should be 17 nanoseconds or greater. The delay should be observed for both positive and negative going transitions, and both should be 17 nanoseconds or greater. If the smaller of the two delays is less than 17 nanoseconds, add a capacitor in location C16 to increase the delay.

Test 2: Tie pins L3 and T83 high. Apply a square wave signal with a period of 240 nanoseconds or greater to pin T31. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin T31 with one channel of 454 oscilloscope. Observe the waveform at pin L2 with the second channel. The delay between the two waveforms, measured from mid-point of each transition, should be 40 nanoseconds or greater. The delay should be observed for both positive and negative going transitions, and both should be 40 nanoseconds or greater. If the smaller of the two delays is less than 40 nanoseconds, the value of capacitor C32 should be increased. If the value of the smaller of the delays is greater than 50 nanoseconds, the value of C32 should be reduced.

CHG.	E.C.O.	DATE	APPR
		12-10-70	
1	0301	11-16-73	<i>4/3</i>

The final capacitor values and the measured delays for both directions of transition should be recorded, along with the serial number of the board.

The circuit board should be carefully inspected to insure that the foregoing procedure has not resulted in damage to the circuit board, particularly in the areas where fresh soldering has taken place. All flux residues should be thoroughly removed.



NOTE 1:
MALE AMPMODU PINS MUST BE
INSTALLED FROM THIS SIDE IN
LOCATIONS MARKED X PRECISELY
AS SHOWN IN DWGS. 200.50D1 AND
200.50D2.
(29 PINS)

NOTE 2:
SEE DRAWING NUMBER 200.50D29
FOR CONNECTOR MOUNTING
ORIENTATION.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		COMPONENT IDENTIFICATION SHIFT FACEPLATE MOTHERBOARD PART NO. 203.3	
				MACROMODULAR PROJECT		APPROVED BY FOR DATE CEM MANUF. 20 Nov 70 CEM MANUF. 7-28-71	
						ENG. DLS DRAWN BY PLL CHECKED MTK.	
CHANGE NO.	DATE	DESCRIPTION				DRAWING NO. 203.3D1 DATE 11-17-70	
2	11-13-70	E.C.O. 0082 CEM					

RESISTORS 15K OHM 5% 1/4WATT CARBON
TWO REQUIRED
R01
R02

SPRAGUE NETWORK LTN-2
ONE REQUIRED
N01

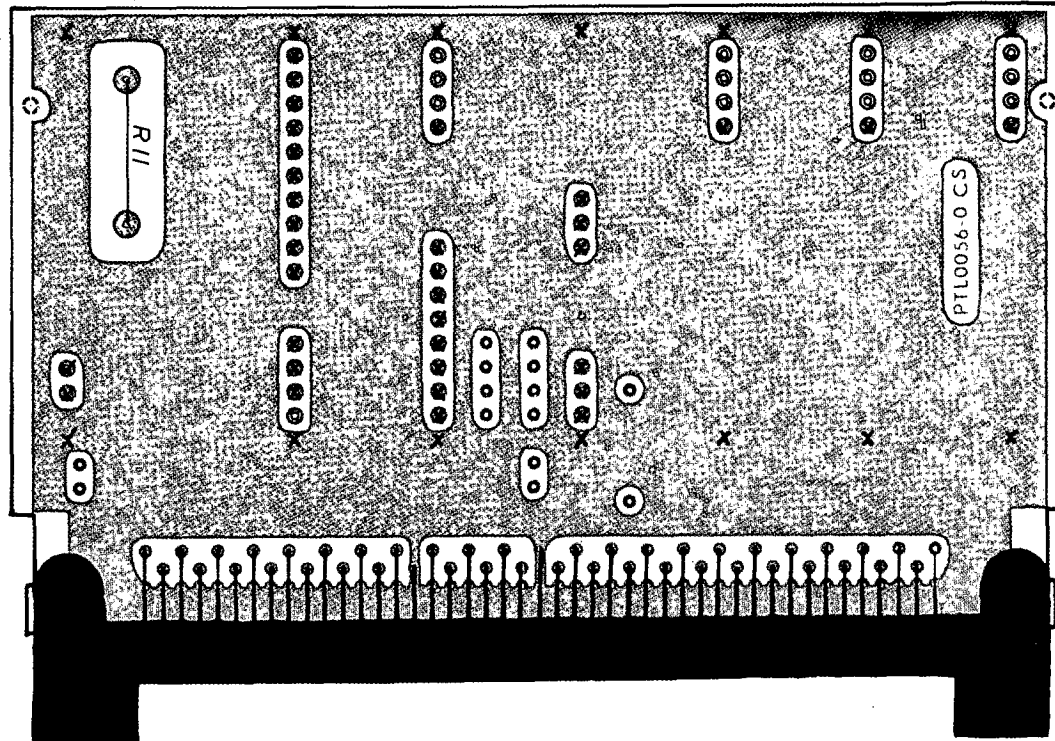
AMP CONNECTOR
583 464-1
ONE REQUIRED

CONNECTOR
AMPMODU NO. 85931-5
29 REQUIRED

CIRCUIT BOARD
PTF0058-1
ONE REQUIRED

2	7-27-71	E.C.O. 0209	WTK
1	10/10/70	E.C.O. 0051	CEM WTK
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST SHIFT FACEPLATE MOTHER BOARD PART NO. 203.3			
APPROVED			ENG
BY	FOR	DATE	REO
CEM	MANUF.	4/7/70	DRAWN BY MBP
CEM	MANUF.	7-28-71	CHECKED WTK
			DRAWING NO. 203.3D2
			DATE 6/24/70

NOTE: AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DRAWINGS 200.50D1 AND 200.50D2.



NOTE: SEE DRAWING NUMBER 200.50D28 FOR CONNECTOR MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI	TITLE COMPONENT IDENTIFICATION SHIFT LATERAL MOTHERBOARD ASSEMBLY PART NO. 203.4 COMPONENT SIDE																		
2	11-24-71	E.C.O. 0232 <i>M.L.P.</i>	MACROMODULAR PROJECT	<table><tr><td colspan="3">APPROVED</td><td>ENG. DLS</td><td rowspan="2">DRAWING NO. 203.4D2</td></tr><tr><td>BY</td><td>FOR</td><td>DATE</td><td>DRAWN BY</td></tr><tr><td><i>com</i></td><td><i>MANUF</i></td><td><i>10/14/70</i></td><td>PLL</td></tr><tr><td><i>com</i></td><td><i>MANUF.</i></td><td><i>7-28-71</i></td><td>CHECKED NTK</td><td>DATE 10/14/70</td></tr></table>	APPROVED			ENG. DLS	DRAWING NO. 203.4D2	BY	FOR	DATE	DRAWN BY	<i>com</i>	<i>MANUF</i>	<i>10/14/70</i>	PLL	<i>com</i>	<i>MANUF.</i>	<i>7-28-71</i>	CHECKED NTK	DATE 10/14/70
APPROVED				ENG. DLS	DRAWING NO. 203.4D2																	
BY	FOR	DATE		DRAWN BY																		
<i>com</i>	<i>MANUF</i>	<i>10/14/70</i>		PLL																		
<i>com</i>	<i>MANUF.</i>	<i>7-28-71</i>	CHECKED NTK	DATE 10/14/70																		
1	11-27-70	E.C.O. 0095 <i>com</i>																				
CHANGE NO.	DATE	DESCRIPTION																				

JUMPERS
SIX REQUIRED

R01
R02
R03
R04
R09
R10

RESISTOR 33.2K OHM 1/8 WATT 1%
ONE REQUIRED
R12

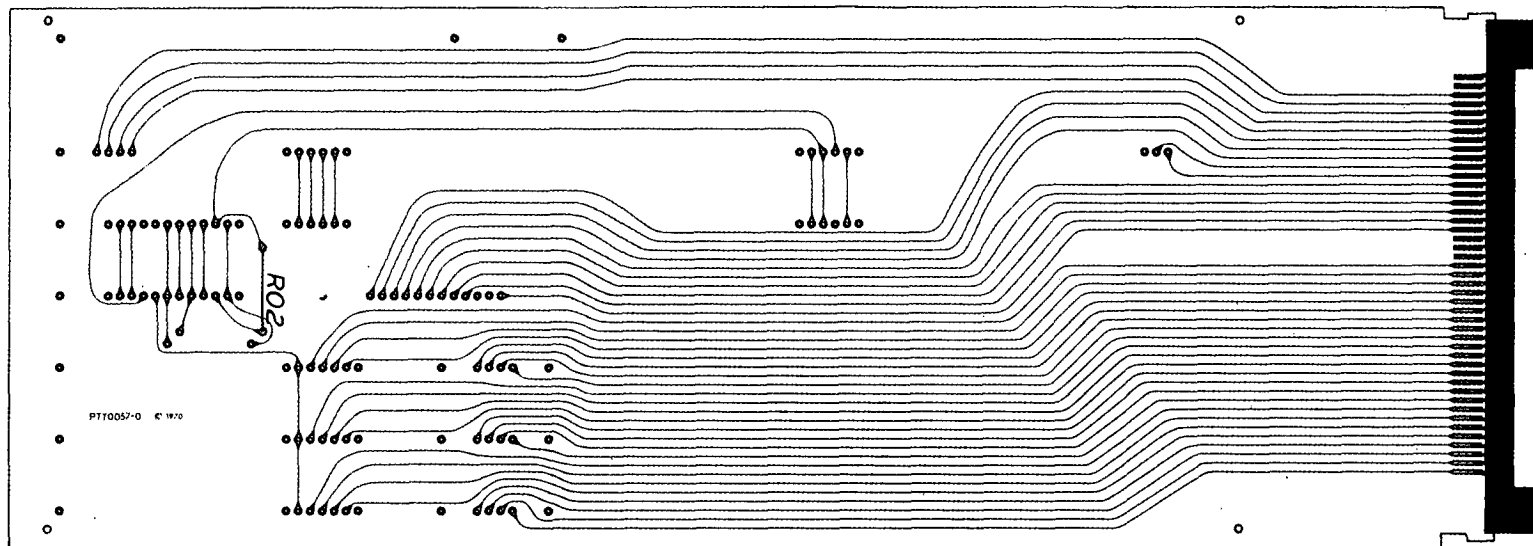
AMP CONNECTOR
583 464-1
ONE REQUIRED

CONNECTORS
AMP MODU NO. 85931-5
FORTY SEVEN REQUIRED

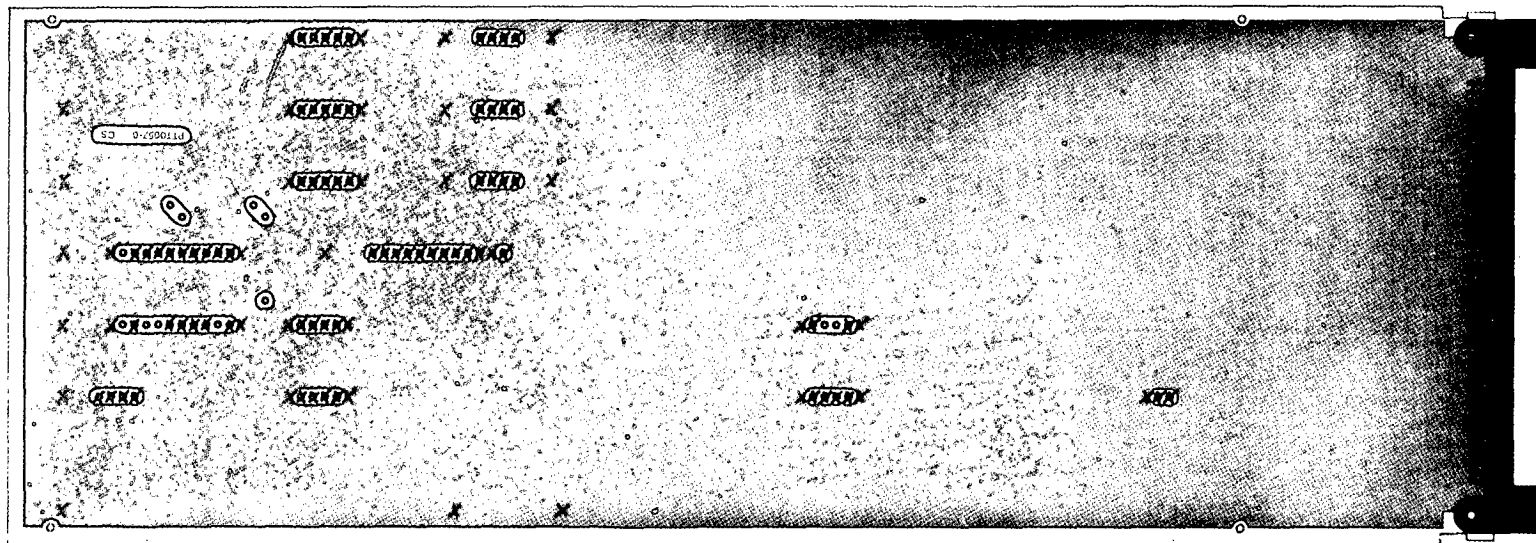
FUSE, BUSSMAN GFA 3/4 AMP
ONE REQUIRED
R11

CIRCUIT BOARD
PTL0056-1
ONE REQUIRED

3	11-24-71	E.C.O. 0232 <i>MLP</i>
2	7-27-71	E.C.O. 0209 <i>MLP</i>
1	10/10/70	E.C.O. 0051 <i>MLK</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST SHIFT LATERAL MOTHER BOARD PART NO. 203.4		
APPROVED		ENG <i>REO</i>
BY	FOR	DATE
<i>CALAN</i>	MANUF.	4/7/70
<i>CALAN</i>	MANUF.	7-28-71
CHECKED	DATE	
<i>MLK</i>	6/24/70	
DRAWING NO.		203.4D3



				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION SHIFT TOP MOTHERBOARD PART NO. 203.5 SIGNAL SIDE				
				MACROMODULAR PROJECT		APPROVED BY FOR DATE COM MANUF 3-28-70 COM MANUF 7-28-71			ENG. DLS DRAWN BY PLL	DRAWING NO. 203.5D1
1	10/10/70	E.C.O. 0051				CHECKED NTK			DATE 3-28-70	
CHANGE NO.	DATE	DESCRIPTION								



NOTE: MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.

NOTE: SEE DRAWING 200.50D26 FOR CONNECTOR
MOUNTING ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION, SHIFT TOP MOTHERBOARD ASSEMBLY COMPONENT SIDE PART NO. 203.5				
			MACROMODULAR PROJECT		APPROVED BY FOR DATE			ENG. DLS DRAWN BY	DRAWING NO. 203.5D2
2 11-27-70 E.C.O. 0095 Cam					Cam MANUF 10/24/70			CHECKED NTK	
1 11-11-70 E.C.O. 0081 Zirus Cam					Cam MANUF. 7-28-71				
CHANGE NO.	DATE	DESCRIPTION							

JUMPERS
ONE REQUIRED
R02

AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTOR
AMPMODU 85931-5
109 REQUIRED

CIRCUIT BOARD
PTT0057-1
ONE REQUIRED

2	7-27-71	E.C.O. 0209	4.1.1
1	10/10/70	E.C.O. 0051	4.1.1
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST SHIFT TOP MOTHER BOARD PART NO. 203.5			
APPROVED		ENG. <i>K.E.O.</i>	DRAWING NO.
BY	FOR	DATE	203.5D3
	MANUF.	4/10/70	
	MANUF.	7-28-71	CHECKED <i>WTK</i>
			DATE 6/24/70

S
H
I
F
T

METALCRAFT "AUTOGRAPH" OR EQUIVALENT
BLANK SIZE: $\frac{1}{4}$ " X 2" SHEARED WITH
SQUARE CORNERS, WHITE LETTERS,
VOGUE BOLD 12 POINT BOLD FACE TYPE CENTERED
TOP, BOTTOM AND SIDES WITH 6 POINT
SPACING ON RUST PMS 152 BACKING,
MANUFACTURED FROM .016 THICK ALUMINUM
WITH SOLVENT ACTIVATED PERMANENT
ADHESIVE BACKING.

NOTE: PANTONE MATCHING SYSTEM (PMS)

COMPUTER SYSTEMS LABORATORY

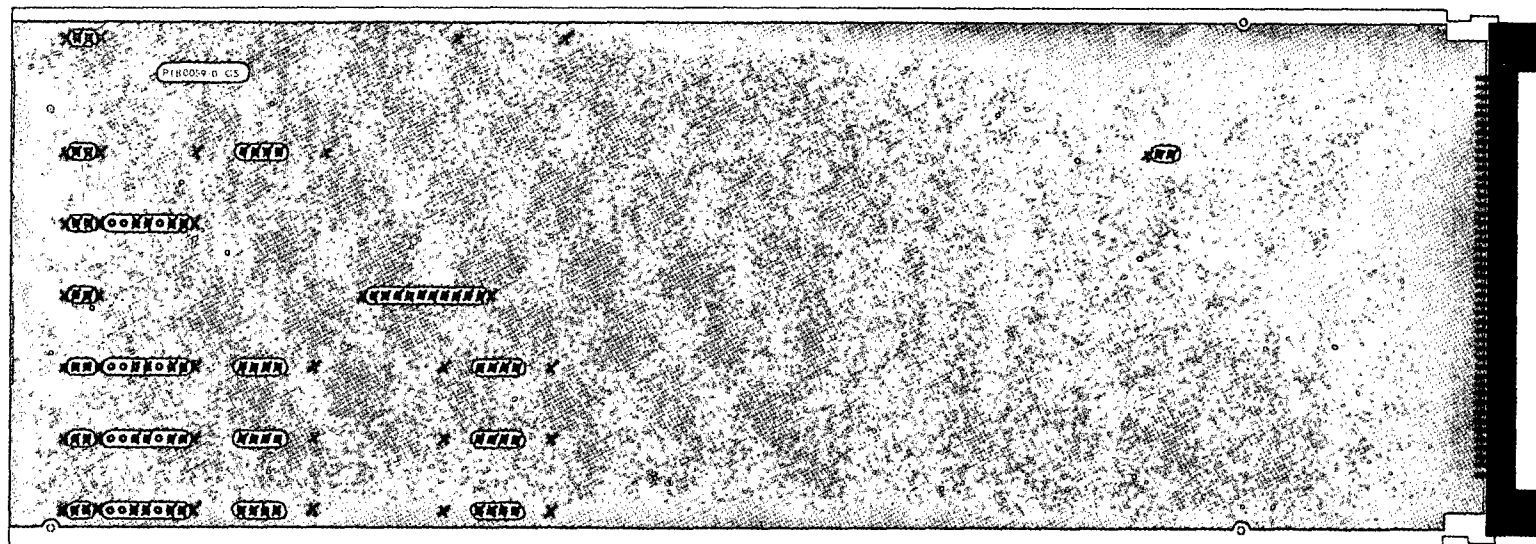
WASHINGTON UNIVERSITY
ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE

IDENTIFICATION LABEL
SHIFT MODULE
PART #203.6

APPROVED			ENG	DRAWING NO.
BY	FOR	DATE	NTK	203.6D
<i>Maw</i>	<i>Prod.</i>	<i>7/28/70</i>	DRAWN BY KM	
			CHECKED <i>Maw</i>	DATE 6-16-70



NOTE: MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.
(104 PINS)

NOTE: SEE DRAWING NUMBER 200.50D27
FOR CONNECTOR MOUNTING
ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			TITLE COMPONENT IDENTIFICATION SHIFT BOTTOM MOTHERBOARD ASSEMBLY PART NO. 203.7		
			MACROMODULAR PROJECT			APPROVED BY <i>Cam</i> FOR <i>MANUF</i> DATE <i>10/13/70</i>		ENG. DLS DRAWN BY <i>MBP</i>
CHANGE NO.	DATE	DESCRIPTION				CHECKED <i>NTK</i>		DATE 10/14/70

AMP CONNECTOR
1-202845-5
ONE REQUIRED

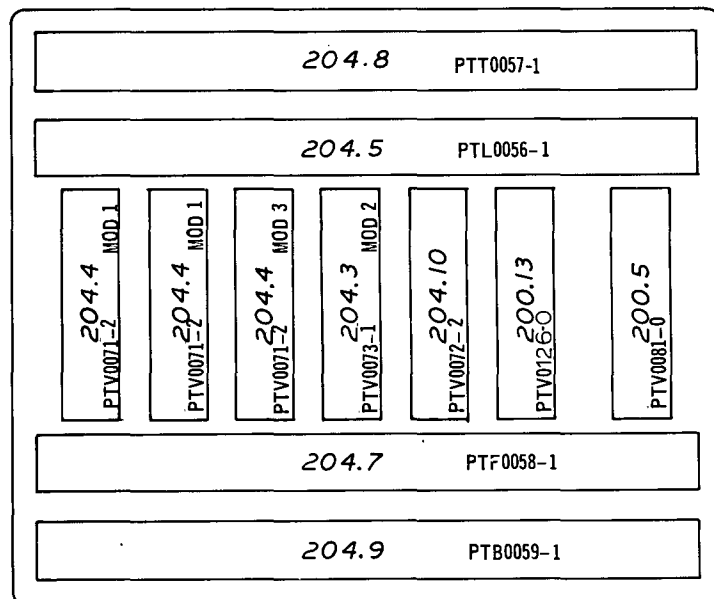
CONNECTORS
AMPMODU NO. 85931-5
ONE HUNDRED FOUR REQUIRED

CIRCUIT BOARD
PTB0059-1
ONE REQUIRED

1.	7-27-71	E.C.O. 0209 <i>NTK</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST BOTTOM MOTHERBOARD ASSEMBLY PART NO. 203.7		
APPROVED		ENG. <i>DLS</i>
BY	FOR	DATE
<i>cam</i>	MANUF.	10/14/70
	MANUF.	7-28-71
DRAWN BY		DRAWING NO.
MBP		203.7D2
CHECKED		DATE
<i>NTK</i>		10/14/70

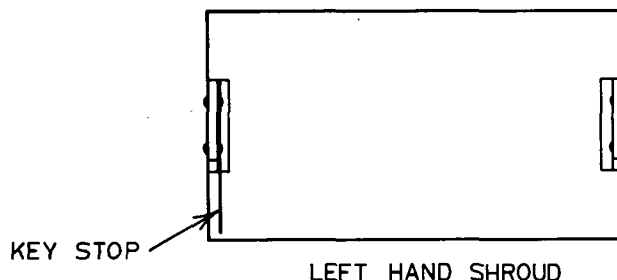
10/29/70

204.6

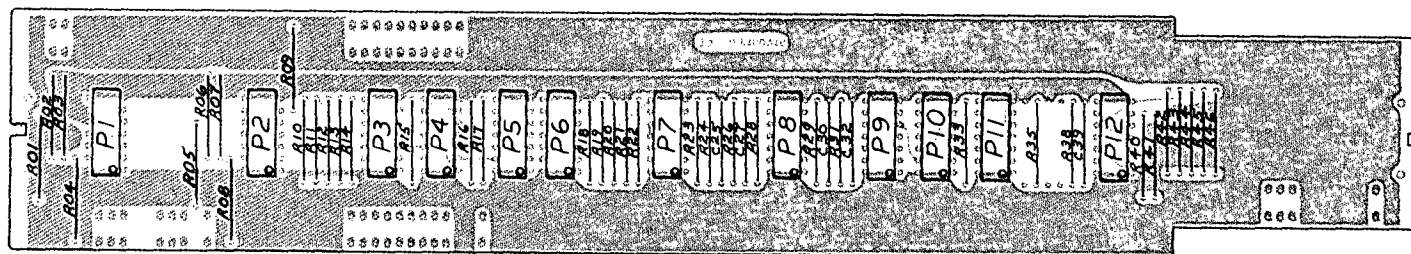


204.6

200.1
ONE CELL CASE
ASSEMBLY



			MACROMODULAR PROJECT			
			TITLE ASSEMBLY SCHEMATIC & PARTS LIST			
			COMPARATOR UNIT MOD. 2			
			PART NO. 204			
CHANGE NO.	DATE	DESCRIPTION	APPROVED		ENG	DRAWING NO.
1	7-27-71	ECO 0211	BY	FOR	WAC	204.0D MOD. 2
					PLP	
					CHECKED	DATE
					NTK	6-3-71
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI						



NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DRAWING 200.50D2

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION COMPARATOR TRANSFER CONTROL BOARD PART NO. 204.3 MOD 2						
					APPROVED BY FOR DATE <i>Cem</i> MANUF. 3-20-70 <i>Cem</i> MANUF. 7/1/71		ENG. DLS DRAWN BY PLL CHECKED NTK		DRAWING NO. 204.3D1 DATE 3-19-70		
CHANGE NO.	DATE	DESCRIPTION	MACROMODULAR PROJECT								
3	2-19-71	E.C.O. 0159 <i>NTK</i> <i>Cem</i>									
2	11-30-70	E.C.O. 0099 <i>NTK</i> <i>Cem</i>									
1	10/20/70	E.C.O. 0061 <i>NTK</i> <i>Cem</i>									

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M04	3	P3 P9 P11
M06	3	P2 P6 P7
M10	5	P1 P4 P5 P8 P12
M11	1	P10

CAPACITORS*

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
33 pf	4	C25 C27 C30 C32
5 pf	1	C39

*DIPPED SILVER MICA 5%

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R1	7	R02 R03 R11 R12 R42 R43 R44
R2	13	R06 R07 R10 R20 R21 R24 R26 R28 R29 R31 R33 R35 R38
R3	15	R01 R04 R05 R08 R09 R13 R14 R15 R17 R19 R22 R23 R40 R41 R45
R4	3	R16 R18 R46

CONNECTORS
AMP MODU NO. 85863-4
47 REQUIRED

CIRCUIT BOARD
PTV0073-1
ONE REQUIRED

NOTE:

R1 = 1.5K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R4 = 15K OHM 5% CARBON COMP.

1	6-2-71	E.C.O. 0194 <i>44S</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST COMPARATOR TRANSFER CONTROL BOARD PART NO. 204.3 MOD 2		
APPROVED		ENG. DLS
BY	FOR	DATE
<i>C.E.</i>	PROD.	6-17-71
CHECKED		DATE
<i>[Signature]</i>		6-4-71

Test Procedure

Compare Module Transfer Control Board #204.3 MOD 2

This board contains 3 critical delays whose proper value must be checked on each board prior to assembly into a Compare Module.

All delay values must be longer than a specified minimum value. If the delay value is excessively large, the operation of the module will be needlessly slowed down. If the delay value is excessively short, the module may perform incorrectly under certain conditions.

Procedure

Test 1: Tie pin L9 high. Apply one phase of a square wave signal with a period of 300 nanoseconds or greater to pin L10 and apply the opposite phase to pin T82. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin L10 with channel one of a 454 oscilloscope. Observe the waveform at pin L11 with the second channel. The delay between waveform at L10 and L11, measured from mid-point of each transition, should be 17 nanoseconds or greater. The delay should be observed for both positive and negative going transitions, and both should be 17 nanoseconds or greater. If the smaller of the two delays is less than 17 nanoseconds, increase the value of C39. If the smaller of the two delays is greater than 22 nanoseconds, the value of C39 should be reduced.

Test 2: Next observe the signal at pin T84 with channel two of the oscilloscope. The delay for both positive and negative transitions should be 22 nanoseconds or greater. If the smaller of the two delays is less than 22 nanoseconds, increase the value of C34 and C36. If the smaller of the two delays is greater than 30 nanoseconds, decrease the value of C34 and C36.

Test 3: Tie pins L9 and L10 low. Apply a square wave signal with a period of 300 nanoseconds or greater to pin T82. The signal should have a rise and fall time not greater than 10 nanoseconds. Observe the waveform at pin

CHG.	E.C.O.	D-TE.	APPR
	Orig.	12-29-70	cam
1	0194	6-2-71	NTK
2	0301	11-16-73	4/5

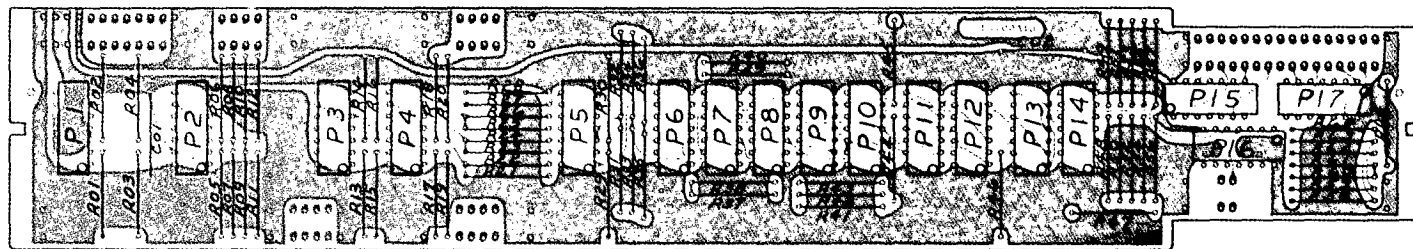
T82 with channel one of a 454 oscilloscope and observe the waveform at pin T84 with the second channel. The delay between the two waveforms, measured from midpoint of each transition, should be 133 nanoseconds or greater for both the positive and negative transtiion. If the shorter of the two delays is less than 133 nanoseconds, increase the value of C25 and C27 to increase the delay. If the shorter of the two delays is greater than 145 nanoseconds, reduce the value of C25 and C27, to decrease the delay.

The final capacitor values and the measured delays for each board should be recorded on the test sheet provided for that board, along with the serial number of the board.

The circuit should be carefully inspected to insure that the foregoing procedure has not resulted in damage to the circuit board, particularly in the areas where fresh soldering has taken place. All flux residues should be thoroughly removed.

	Orig.	12-29-70	em
1	0194	6-2-71	nm
2	0301	11-16-73	4/S

NOTE:
AT TIME OF INSTALLATION OF BOARD,
MODIFY AS FOLLOWS-
MOD 1: REMOVE R31
MOD 3: REMOVE R33



NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DRAWING 200.50D2.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI	COMPONENT IDENTIFICATION COMPARATOR DATA BOARD PART NO. 204.4			
CHANGE NO	DATE	DESCRIPTION		APPROVED		ENG.	DRAWING NO.
2	11-30-70	E.C.O. 0099	MACROMODULAR PROJECT	BY	FOR	DATE	204.401
1	10-30-70	NEW LAYOUT E.C.O. 0074		Cem	MANUF.	25 Nov. 70	
				Cem	MANUF.	7/1/71	
						CHECKED MTK	DATE 10-30-70

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M06	5	P1 P3 P4 P10 P16
M10	1	P11
M11	1	P13
M12	1	P14
M20	2	P15 P17
M21	4	P6 P7 P8 P9
M30	3	P2 P5 P12

CAPACITORS*

TYPE	REQUIRED	LOCATION
10,000 pf	2	C01 C02

RESISTORS

TYPE	REQUIRED	LOCATION
R0	2	R04 R31
R1	21	R05 R07 R09 R11 R30 R34 R36 R37 R38 R39 R40 R42 R43 R49 R51 R53 R55 R58 R59 R60 R65
R2	12	R01 R14 R16 R18 R20 R21 R23 R25 R28 R44 R56 R57
R3	27	R02 R06 R08 R10 R12 R13 R15

RESISTORS (Cont)

TYPE	REQUIRED	LOCATION
R3	(Cont)	R17 R19 R22 R24 R26 R27 R29 R33 R35 R45 R46 R47 R48 R50 R52 R54 R61 R62 R63 R64
R4	3	R03 R32 R41

CONNECTORS
AMP MODU NO. 85863-4
58 REQUIRED

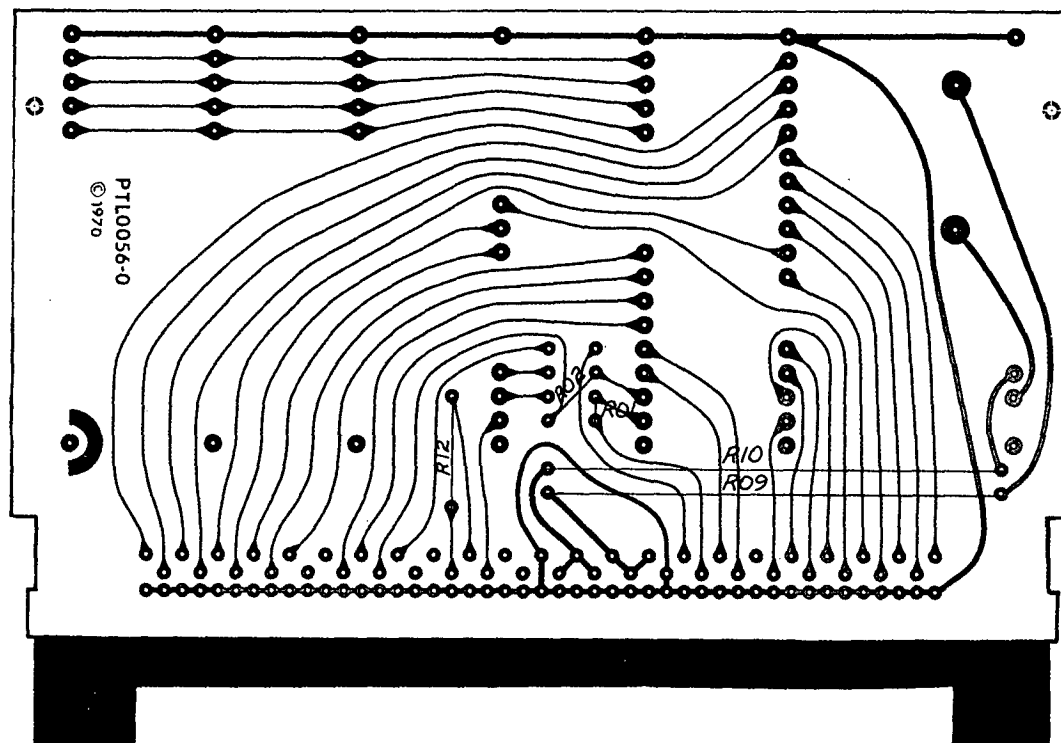
CIRCUIT BOARD
PTV0071-2
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R4 = 15K OHM 5% 1/4WATT CARBON COMP.

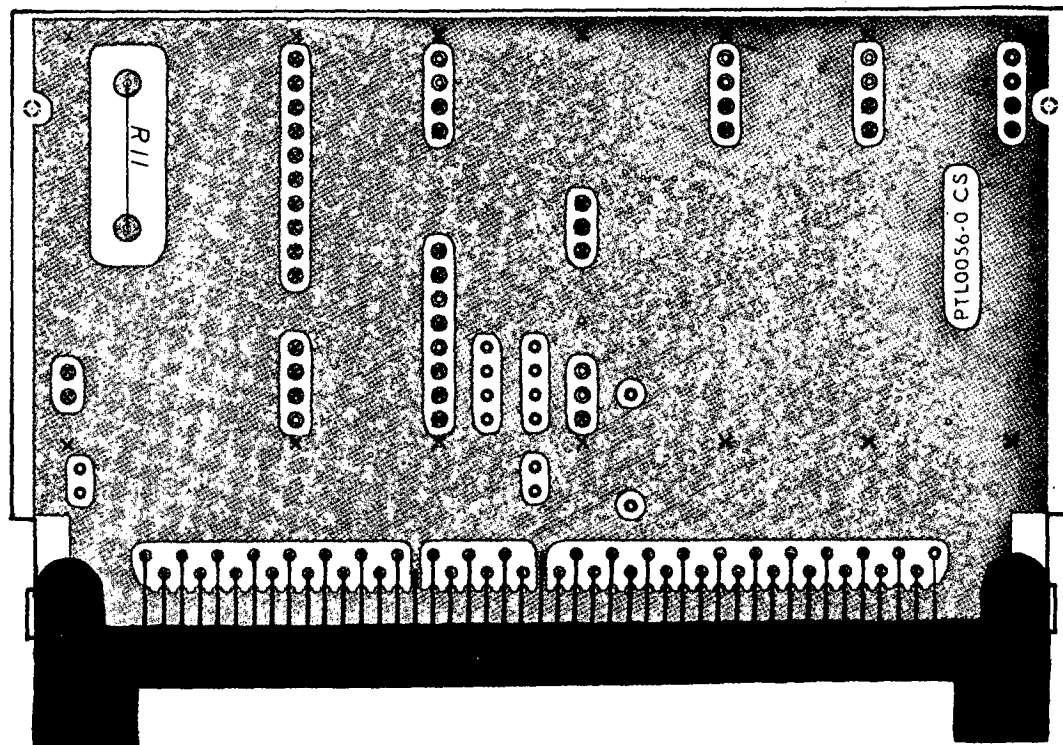
*CK-103 SPRAGUE
CERAMIC DISC. 50 WVDC

3	7-2-71	E.C.O. 0199
2	5-28-71	E.C.O. 0186
1	10-30-70	NEW LAYOUT E.C.O. 0074
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST COMPARATOR DATA BOARD PART NO. 204.4		
APPROVED		ENG. REQ
BY	FOR	DATE
DRAWN BY MBP		DRAWING NO. 204.4D2
CHECKED		DATE
		10-30-70



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION COMPARATOR LATERAL MOTHERBOARD SIGNAL SIDE PART NO. 204.5			
					APPROVED BY FOR DATE <i>Cem</i> <i>MANUF</i> <i>3 Apr 70</i> <i>Cem</i> <i>MANUF</i> <i>7/1/71</i>			
1 11-23-70 E.C.O. 0083 <i>Cem</i>			MACROMODULAR PROJECT		ENG. <i>DLS</i> DRAWN BY <i>DHO</i>		DRAWING NO. 204.5D1	
CHANGE NO. DATE DESCRIPTION					CHECKED <i>NTK</i> DATE 3-27-70			

NOTE:
SEE DRAWING NUMBER 200.50D28
FOR CONNECTOR ORIENTATION.



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION COMPARATOR LATERAL MOTHERBOARD ASSEMBLY COMPONENT SIDE PART NO. 204.5				
2	11-24-71	E.C.O.	0232	MLP	APPROVED			ENG. DLS	DRAWING NO. 204.5D2
1	2-24-71	E.C.O.	0161	HTK Cam	BY Cam	FOR MANUF.	DATE 11-27-70	DRAWN BY DHO	
					Cam	MANUF.	7/1/71	CHECKED NTK	
CHANGE NO.	DATE	DESCRIPTION			MACROMODULAR PROJECT				DATE 11-23-70

JUMPERS
FOUR REQUIRED
R01
R02
R09
R10

RESISTOR 25.5K OHM 1/8WATT 1%
ONE REQUIRED
R12

AMP CONNECTOR
583 464-1
ONE REQUIRED

CONNECTOR
AMPMODU NO. 85931-5
FORTY-SEVEN REQUIRED

FUSE BUSSMAN GFA- $\frac{1}{4}$ AMP
ONE REQUIRED
R11

CIRCUIT BOARD
PTL0056-1
ONE REQUIRED

4	11-24-71	E.C.O. 0232	MLP
3	7-2-71	E.C.O. 0199	
2	2-24-71	E.C.O. 0161	
1	11-16-70	E.C.O. 0083	
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST COMPARATOR LATERAL MOTHER BOARD PART NO. 204.5			
APPROVED		ENG.	DRAWING NO.
BY	FOR	DATE	204.5D3
	MANUF.	4/7/70	
		DRAWN BY	DATE
		MBP	
		CHECKED	6/24/70
		NTK	

C
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M
P
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R
E

METALCRAFT "AUTOGRAPH" OR EQUIVALENT:
 BLANK SIZE: 4" X 2" SHEARED WITH
 SQUARE CORNERS, BLACK LETTERS,
 VOGUE BOLD 12 POINT BOLD FACE TYPE CENTERED
 TOP, BOTTOM AND SIDES WITH 6 POINT
 SPACING ON YELLOW PMS 109 BACKING.
 MANUFACTURED FROM .016 THICK ALUMINUM
 WITH SOLVENT ACTIVATED PERMANENT
 ADHESIVE BACKING.

NOTE: PANTONE MATCHING SYSTEM (PMS)

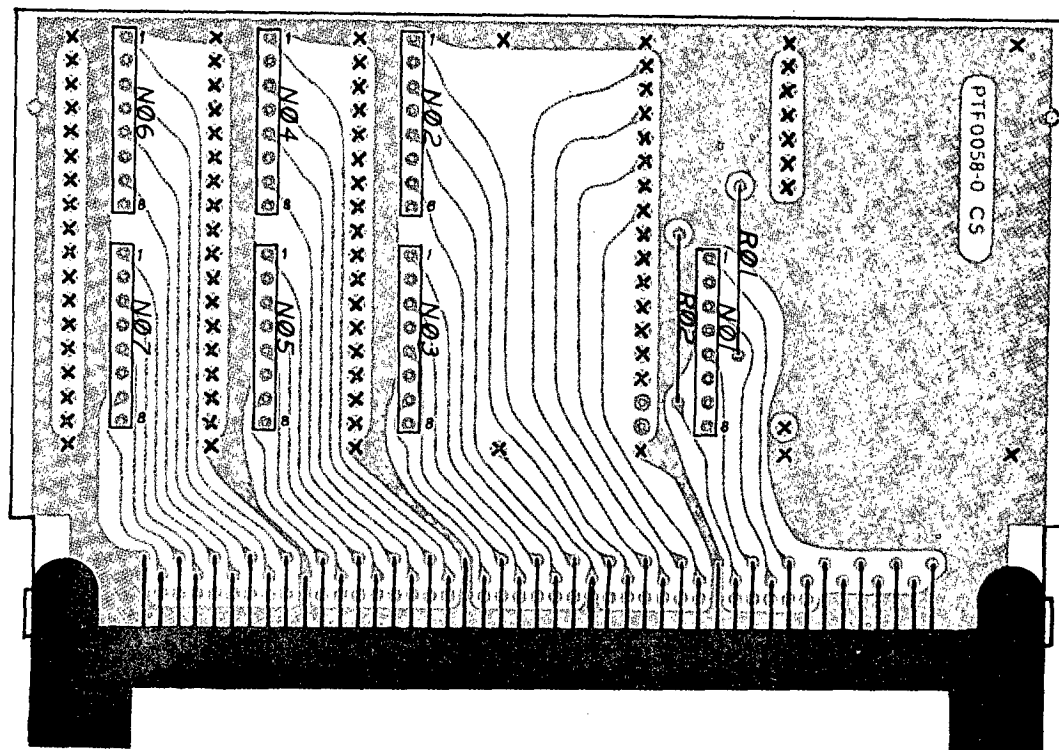
COMPUTER SYSTEMS LABORATORY
 WASHINGTON UNIVERSITY
 ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE

IDENTIFICATION LABEL
 COMPARE MODULE
 PART #204.6

APPROVED			ENG	DRAWING NO. 204.6D
BY	FOR	DATE	NTK	
Maw	Prod.	7/28/70	DRAWN BY KM	DATE 6-16-70
			CHECKED Maw	

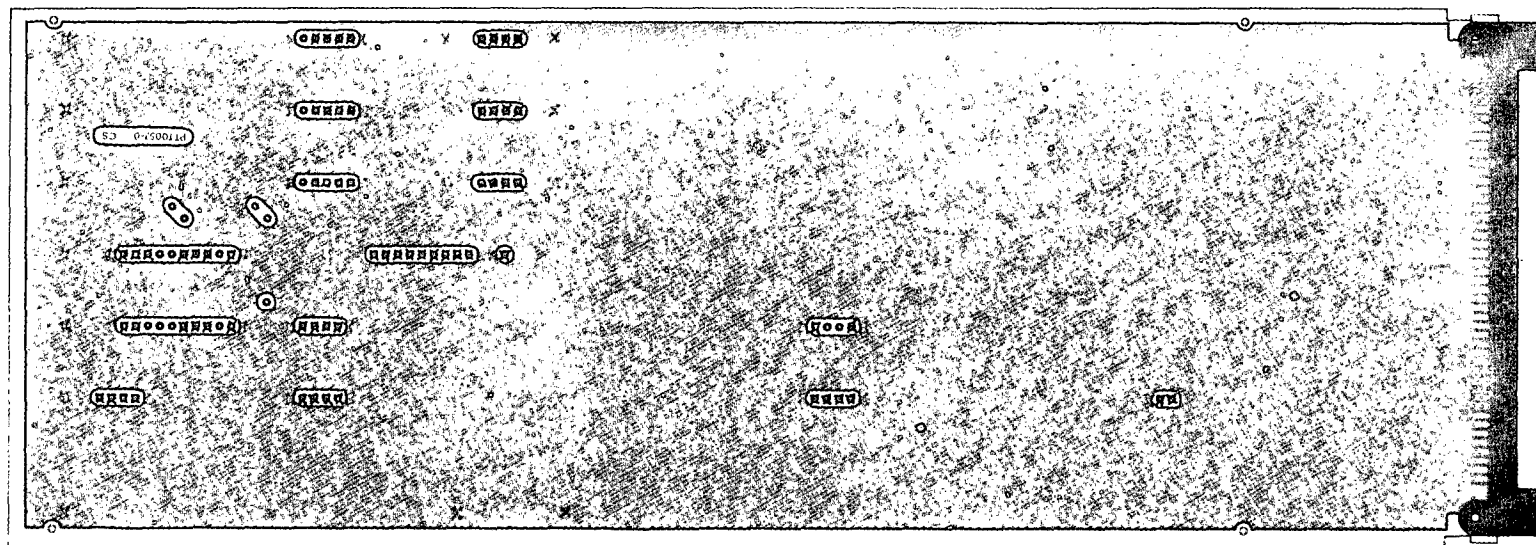


NOTE:
MALE AMPMODU PINS MUST BE INSTALLED
FROM THIS SIDE IN LOCATIONS MARKED X
PRECISELY AS SHOWN IN DRAWINGS
200.50D1 AND 200.50D2.

NOTE:
SEE DRAWING NUMBER 200.50D29
FOR CONNECTOR ORIENTATION.

		COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION COMPARATOR FACEPLATE MOTHERBOARD PART NO. 204.7			
				APPROVED BY FOR DATE CEM MANUF. 11-27-70 CEM MANUF. 7/1/71		ENG. DLS DRAWN BY DHO CHECKED NTK	
CHANGE NO. DATE DESCRIPTION		MACROMODULAR PROJECT					

2	7-2-71	E.C.O. 0199
1	5-28-71	E.C.O. 0191
CHANGE NO.	DATE	DESCRIPTION
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>		
<p align="center">MACROMODULAR PROJECT</p>		
TITLE	PARTS LIST COMPARATOR FACEPLATE MOTHERBOARD PART NO. 204.7	
APPROVED		ENG.
BY	FOR	DATE
CL...	...	11-27-71
CL...	...	11-1-71
		CHECKED
		DATE



NOTE 1:
MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.

NOTE 2:
SEE DRAWING NUMBER 200.50D26
FOR CONNECTOR ORIENTATION.

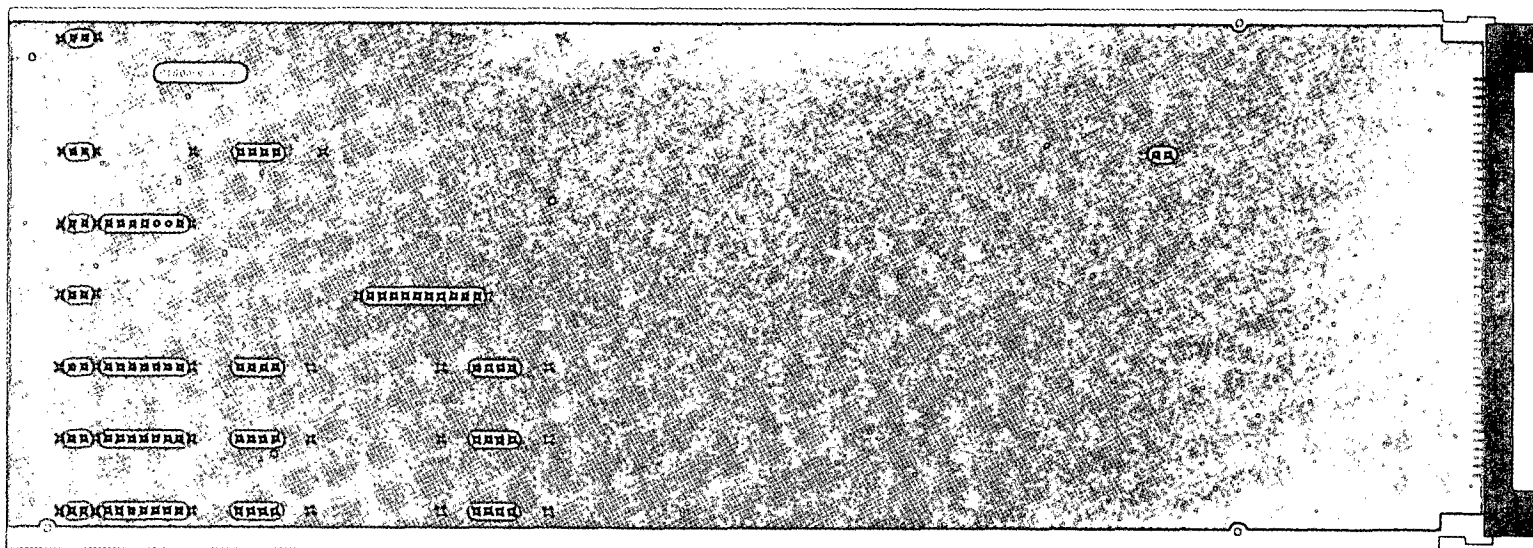
			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		COMPONENT IDENTIFICATION COMPARATOR TOP MOTHERBOARD ASSEMBLY PART NO. 204.8				
			MACROMODULAR PROJECT		APPROVED			ENG. DLS	DRAWING NO. 204.8D1
					BY CEM	FOR MANUF.	DATE 11-27-70	DRAWN BY DHO	
					CEM	MANUF.	7/1/71	CHECKED NTK	DATE 11-16-70
CHANGE NO.	DATE	DESCRIPTION							
2	5-28-71	E.C.O. 0189 NTK, CEM							
1	11-30-70	E.C.O. 0099 CEM							

AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTORS
AMPMODU HO. 85931-5
ONE HUNDRED FOUR REQUIRED

CIRCUIT BOARD
PTT0057-1
ONE REQUIRED

3	7-2-71	E.C.O. 0199
2	5-28-71	E.C.O. 0189
1	11-30-70	E.C.O. 0099
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST COMPARATOR TOP MOTHERBOARD ASSEMBLY PART NO. 204.8		
APPROVED		ENG.
BY	FOR	DATE
CON	MANUF.	11-24-70
CON	MANUF.	11-24-70
DRAWN BY		DRAWING NO.
MBP		204.8D2
CHECKED		DATE
CON		11/24/70



NOTE 1:
SEE DRAWING NUMBER 200.50D27
FOR CONNECTOR ORIENTATION.

NOTE 2:
MALE AMPMODU PINS MUST BE INSTALLED FROM THIS SIDE
IN LOCATIONS MARKED X PRECISELY AS SHOWN IN DWGS.
200.50D1 AND 200.50D2.

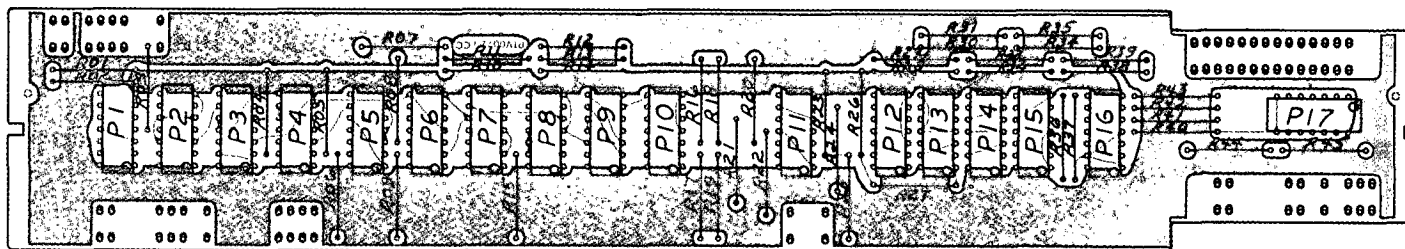
				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION COMPARATOR BOTTOM MOTHERBOARD ASSEMBLY PART NO. 204.9			
				MACROMODULAR PROJECT		APPROVED BY FOR DATE Cam MANUF. 11-17-70 Cam MANUF. 7/1/71		ENG. DLS DRAWN BY DHO CHECKED NTK	DRAWING NO. 204.9D1 DATE 11-16-70
CHANGE NO.	DATE	DESCRIPTION							
1	1-4-71	E.C.O. 0140		<i>NTK</i> <i>Cam</i>					

AMP CONNECTOR
1-202845-5
ONE REQUIRED

CONNECTORS
AMP MODU NO. 85931-5
ONE HUNDRED FOURTEEN REQUIRED

CIRCUIT BOARD
PTB0059-1
ONE REQUIRED

2	7-2-71	E.C.O. 0199
1	1-5-71	E.C.O. 0140
CHANGE NO.	DATE	DESCRIPTION
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>		
<p align="center">MACROMODULAR PROJECT</p>		
<p>TITLE PARTS LIST BOTTOM MOTHERBOARD ASSEMBLY PART NO. 204.9</p>		
APPROVED		ENG.
BY	FOR	DATE
		DRAWN BY MBP
		CHECKED
		DATE 11/24/70
		DRAWING NO. 204.9D2



NOTE: INSTALL FEMALE AMPMODU
CONNECTORS EXACTLY AS
SHOWN ON DRAWING 200.5002.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		COMPONENT IDENTIFICATION COMPARATOR CONTROL BOARD PART NO. 204.10					
				MACROMODULAR PROJECT		APPROVED BY FOR DATE Cam MANUF. 25 Nov 70 Cam MANUF. 7/1/71			ENG. REO DRAWN BY PLL	DRAWING NO. 204.10D1	
2	11-30-70	E.C.O. 0099				Cam					
1	10/30/70	NEW LAYOUT E.C.O. 0074				Cam					
CHANGE NO.	DATE	DESCRIPTION				CHECKED MTK DATE 10-30-70					

INTEGRATED CIRCUITS

TYPE	REQUIRED	LOCATION
M02	1	P4
M04	1	P8
M05	1	P6
M06	2	P15 P16
M10	5	P2 P10 P11 P13 P14
M11	4	P1 P3 P7 P9
M20	1	P17
M31	2	P5 P12

RESISTORS

TYPE	REQUIRED	LOCATION
R0	6	R01 R02 R03 R15 R22 R24
R1	10	R08 R10 R11 R14 R16 R18 R20 R23 R26 R45
R2	10	R04 R05 R28 R29 R30 R31 R36 R37 R40 R41
R3	11	R06 R07 R09 R12 R13 R17 R19 R21 R25 R27 R44

RESISTORS (cont)

TYPE	REQUIRED	LOCATION
R5	8	R32 R33 R34 R35 R38 R39 R42 R43

CONNECTORS
AMP MODU NO. 85863-4
55 REQUIRED

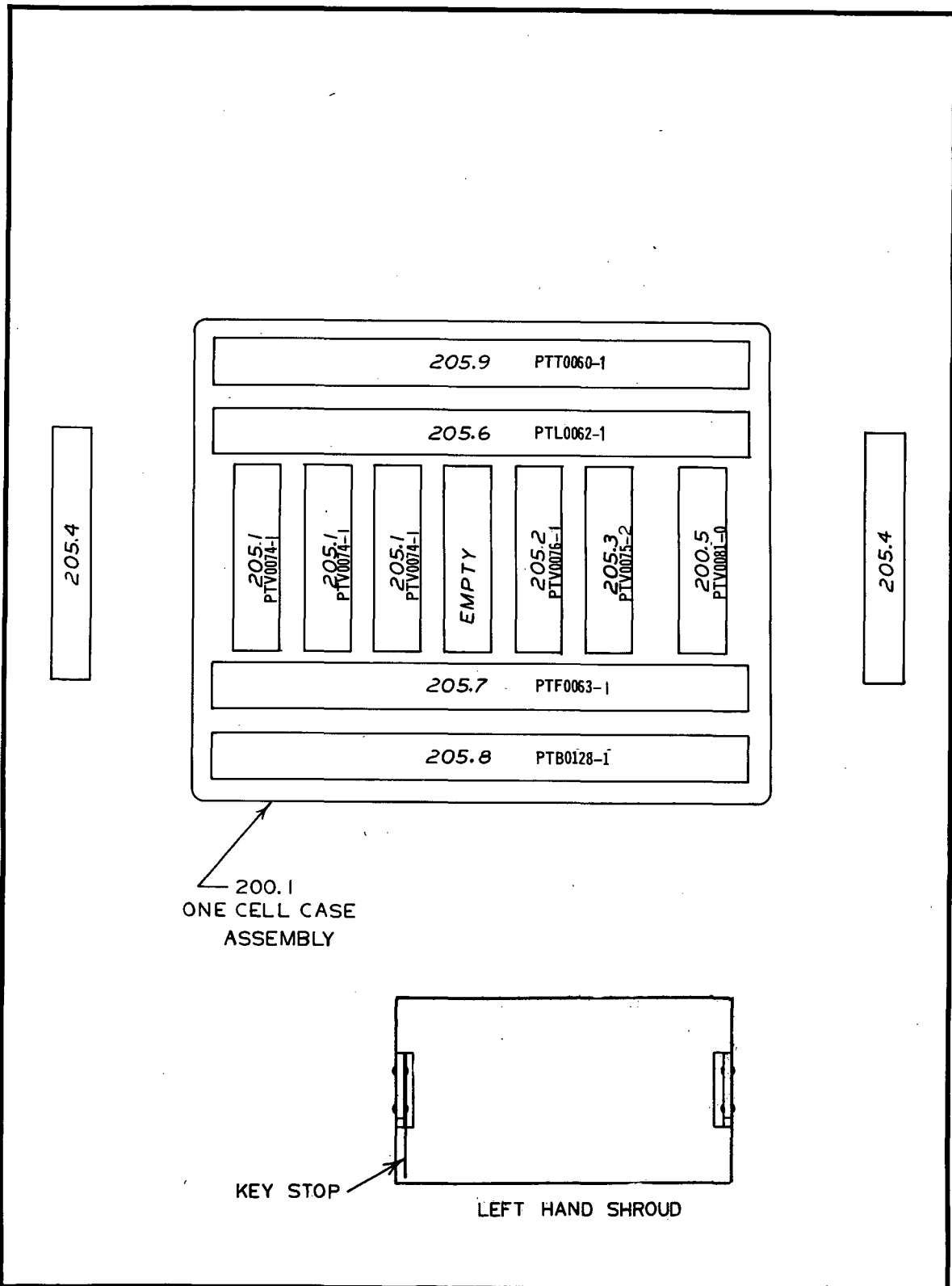
CIRCUIT BOARD
PTV0072-2
ONE REQUIRED

NOTE:

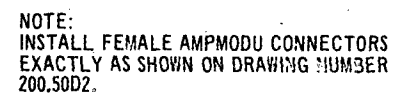
R0 = JUMPERS
R1 = 1.5K OHM 1% FILM RESISTOR
R2 = 750 OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R5 = 57.6 OHM 1% FILM RESISTOR

4	7-2-71	E.C.O. 0199	
3	5-28-71	E.C.O. 0187	
2	11-30-70	ECO 0099	
1	10/30/70	NEW LAYOUT E.C.D. 0074	
CHANGE NO.	DATE	DESCRIPTION	
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>			
<p align="center">MACROMODULAR PROJECT</p>			
<p>TITLE PARTS LIST COMPARATOR CONTROL BOARD PART NO. 204.10</p>			
APPROVED			ENG. REO
BY	FOR	DATE	DRAWING NO.
COM	MANUF.	11/7/70	204.10D2
COM	MAINT.	7/1/71	
CHECKED	DATE		
MTK	10-30-70		

110670



4	9-9-71	E.C.O. 0226	NTK	MACROMODULAR PROJECT TITLE ASSEMBLY SCHEMATIC & PARTS LIST REGISTER UNIT PART NO. 205			
3	7-27-71	E.C.O. 0212	NTK				
2	11-9-70	E.C.O. 0079	NTK				
1	4-17-70	CHANGED TOP M.B.	NTK				
CHANGE NO.	DATE	DESCRIPTION		APPROVED		ENG	DRAWING NO.
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI				BY NTK	FOR D. J. [Signature]	DATE 11-27-71	WAC DRAWN BY PLL
				CHECKED NTK	DATE 3-28-70		



			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI	<div>TITLE COMPONENT IDENTIFICATION REGISTER DATA BOARD PART NO. 205.1</div> <table><tr><td colspan="3">APPROVED</td><td>ENG.</td><td rowspan="3">DRAWING NO. 205.1D1</td></tr><tr><td>BY</td><td>FOR</td><td>DATE</td><td>DLS</td></tr><tr><td>Cem</td><td>MANUF</td><td>3/27/70</td><td>DRAWN BY PLL</td></tr><tr><td>Cem</td><td>MANUF.</td><td>9-15-71</td><td>CHECKED NTK</td><td>DATE 3-26-70</td></tr></table>	APPROVED			ENG.	DRAWING NO. 205.1D1	BY	FOR	DATE	DLS	Cem	MANUF	3/27/70	DRAWN BY PLL	Cem	MANUF.	9-15-71	CHECKED NTK	DATE 3-26-70
APPROVED			ENG.	DRAWING NO. 205.1D1																		
BY	FOR	DATE	DLS																			
Cem	MANUF	3/27/70	DRAWN BY PLL																			
Cem	MANUF.	9-15-71	CHECKED NTK	DATE 3-26-70																		
2	11-9-70	E.C.O. 0079	MACROMODULAR PROJECT																			
1	8-19-70	Added Polarity Indicators E.C.O. 29																				
CHANGE NO.	DATE	DESCRIPTION																				

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M01B	4	P6 P7 P8 P9
M04	1	P1
M10	2	P2 P3
M16	2	P4 P5

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R1	7	R04 R06 R08 R10 R12
R3	5	R02 R03 R01 R05 R07 R09 R11
R5	16	R13 R14 R15 R16 R17 R18 R19 R20 R21 R22 R23 R24 R25 R26 R27 R28

CONNECTORS
AMPMODU NO. 85863-4
45 REQUIRED

CIRCUIT BOARD
PTV0074-1
ONE REQUIRED

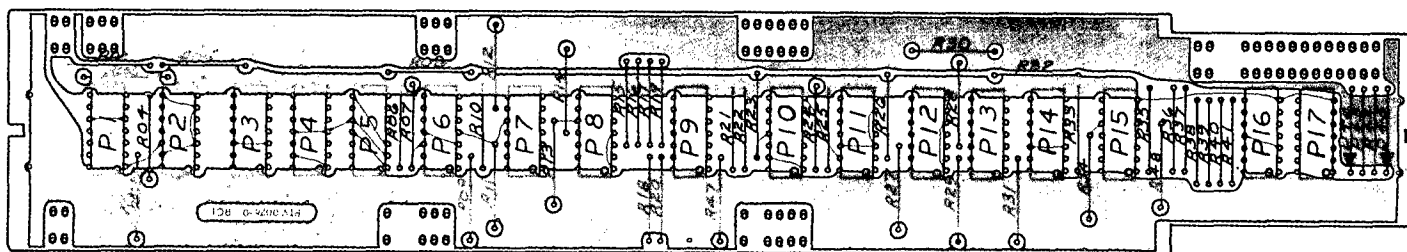
NOTE:

R1 = 1.5 K OHM 1% FILM RESISTOR

R3 = 121 OHM 1% FILM RESISTOR

R5 = 57.6 OHM 1% FILM RESISTOR

2	9-9-71	E.C.O. 0226
1	9-9-70	CHG CIRCUIT TYPE E.C.O. 0034
CHANGE NO.	DATE	DESCRIPTION
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>		
<p align="center">MACROMODULAR PROJECT</p>		
<p>TITLE PARTS LIST REGISTER DATA BOARD PART NO. 205.1</p>		
APPROVED		ENG.
BY	FOR	DATE
CORR.	MANUF.	5/15/70
CORR.	MANUF.	5/15/70
CHECKED		DATE
CORR.		6/17/70
DRAWN BY		DRAWING NO.
MBP		205.1D2



NOTE:
INSTALL FEMALE AMPMODU CONNECTORS
EXACTLY AS SHOWN ON DRAWING NUMBER
200,50D2.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION REGISTER CONTROL BOARD NO. 1 PART NO. 205.2			
						MACROMODULAR PROJECT		APPROVED BY <i>Cem</i> FOR <i>MANUP</i> DATE <i>3/27/70</i>	
CHANGE NO. 2 1	DATE 11-9-70 5-12-70	DESCRIPTION E.C.O. 0079 <i>NTK Cem</i> ADDED PIN NO. 1 INDICATORS						CHECKED <i>Cem</i> <i>MANUF.</i> <i>9-15-71</i>	

INTEGRATED CIRCUITS		
TYPE	REQUIRED	LOCATION
M01	2	P7 P15
M08	1	P5
M10	6	P1 P8 P10 P12 P13 P16
M11	4	P2 P3 P11 P14
M16	1	P9
M20	1	P17
M30	1	P6
M31	1	P4

RESISTORS		
TYPE	REQUIRED	LOCATION
R0	7	R08 R11 R12 R13 R27 R29 R34
R1	15	R01 R04 R06 R10 R14 R16 R17 R19 R21 R22 R25 R32 R33 R35 R48
R3	14	R02 R03 R05 R07 R09 R15 R18 R20 R23 R24 R26 R30 R31 R47
R4	3	R28 R43 R44

RESISTORS (cont)		
TYPE	REQUIRED	LOCATION
R5	6	R36 R37 R38 R39 R40 R41

DIODES 1N3604
TWO REQUIRED
D42
D45

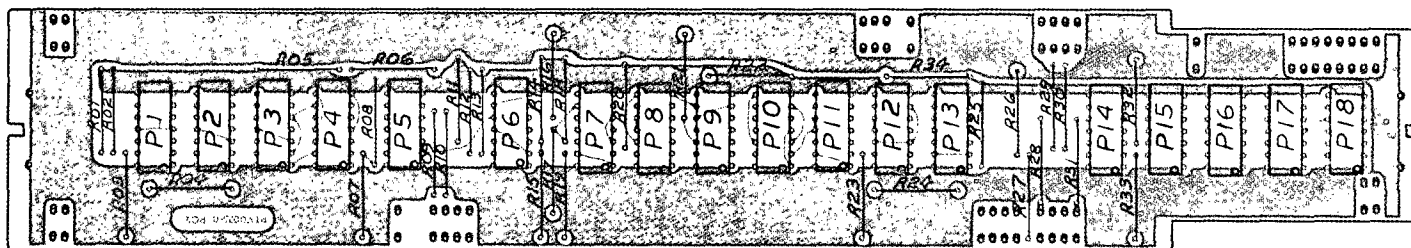
CONNECTORS
AMPMODU NO. 85863-4
56 REQUIRED

CIRCUIT BOARD
PTV0076-1
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5 K OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R4 = 15K 5% CARBON COMP.
R5 = 57.6 OHM 1% FILM RESISTOR

2	9-9-71	E.C.O. 0226
1	9-9-70	CHG CIRCUIT TYPE E.C.O. 0033 <i>Chen</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST REGISTER CONTROL BOARD NO. 1 PART NO. 205.2		
APPROVED		ENG. <i>CSM</i>
BY <i>CSM</i>	FOR MANUF.	DATE 5/15/70
DRAWN BY MBP		DRAWING NO. 205.2D2
CHECKED <i>CSM</i>	DATE 6/17/70	



NOTE:
INSTALL FEMALE AMPMODU CONNECTORS
EXACTLY AS SHOWN ON DRAWING NUMBER
200.50D2.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION REGISTER CONTROL BOARD NO. 2 PART NO. 205.3					
						APPROVED BY <i>Cam</i> FOR <i>MANUP</i> DATE <i>3/27/70</i>		ENG <i>DLS</i> DRAWN BY <i>PLL</i>		DRAWING NO. 205.3D1	
2	11-9-70	E.C.O. 0079		MACROMODULAR PROJECT		BY <i>Cam</i> FOR <i>MANUF.</i> DATE <i>9-15-71</i>		CHECKED <i>NTK</i>		DATE 3-26-70	
1	8-19-70	Added Polarity Indicators E.C.O. 30									
CHANGE NO.	DATE	DESCRIPTION									

INTEGRATED CIRCUITS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
M01B	4	P14 P15 P16 P17
M10	4	P6 P8 P9 P13
M11	4	P2 P3 P10 P12
M16	1	P5
M20	1	P18
M30	1	P1
M31	3	P4 P7 P11

RESISTORS

<u>TYPE</u>	<u>REQUIRED</u>	<u>LOCATION</u>
R0	1	R09
R1	11	R01 R02 R05 R08 R11 R12 R14 R18 R20 R22 R24
R3	20	R03 R04 R06 R07 R10 R13 R15 R16 R17 R19 R21 R23 R26 R27 R28 R29 R30 R31 R32 R33
R4	2	R25 R34

CONNECTORS
AMPMODU NO. 85863-4
52 REQUIRED

CIRCUIT BOARD
PTV0075-1
ONE REQUIRED

NOTE:

R0 = JUMPERS
R1 = 1.5 K OHM 1% FILM RESISTOR
R3 = 121 OHM 1% FILM RESISTOR
R4 = 15K OHM 5% CARBON COMP.

1	9-9-71	E.C.O. 0226	
CHANGE NO.	DATE	DESCRIPTION	
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI			
MACROMODULAR PROJECT			
TITLE PARTS LIST REGISTER CONTROL BOARD NO. 2 PART NO. 205.3			
APPROVED		ENG.	DRAWING NO.
BY	FOR	DATE	
COLE	MANUF.	5/15/70	205.3D2
COLE	CHECKED	6/17/70	

R
E
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R

METALCRAFT "AUTOGRAPH" OR EQUIVALENT:
 BLANK SIZE: $\frac{1}{4}$ " X 2" SHEARED WITH
 SQUARE CORNERS. BLACK LETTERS,
 VOGUE BOLD 12 POINT BOLD FACE TYPE
 CENTERED TOP, BOTTOM AND SIDES WITH
 6 POINT SPACING ON IVORY PMS 134
 BACKING. MANUFACTURED FROM .016
 THICK ALUMINUM WITH SOLVENT ACTIVATED
 PERMANENT ADHESIVE BACKING.

NOTE: PANTONE MATCHING SYSTEM (PMS)

COMPUTER SYSTEMS LABORATORY

WASHINGTON UNIVERSITY

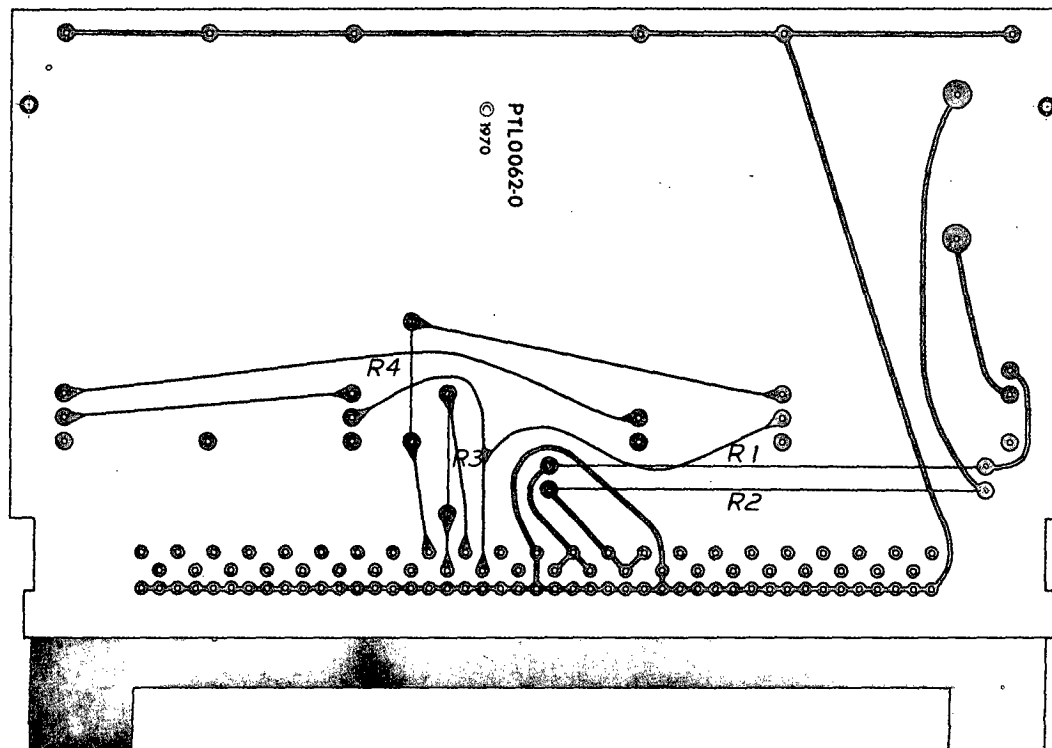
ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE

IDENTIFICATION LABEL
 REGISTER MODULE
 PART #205.4

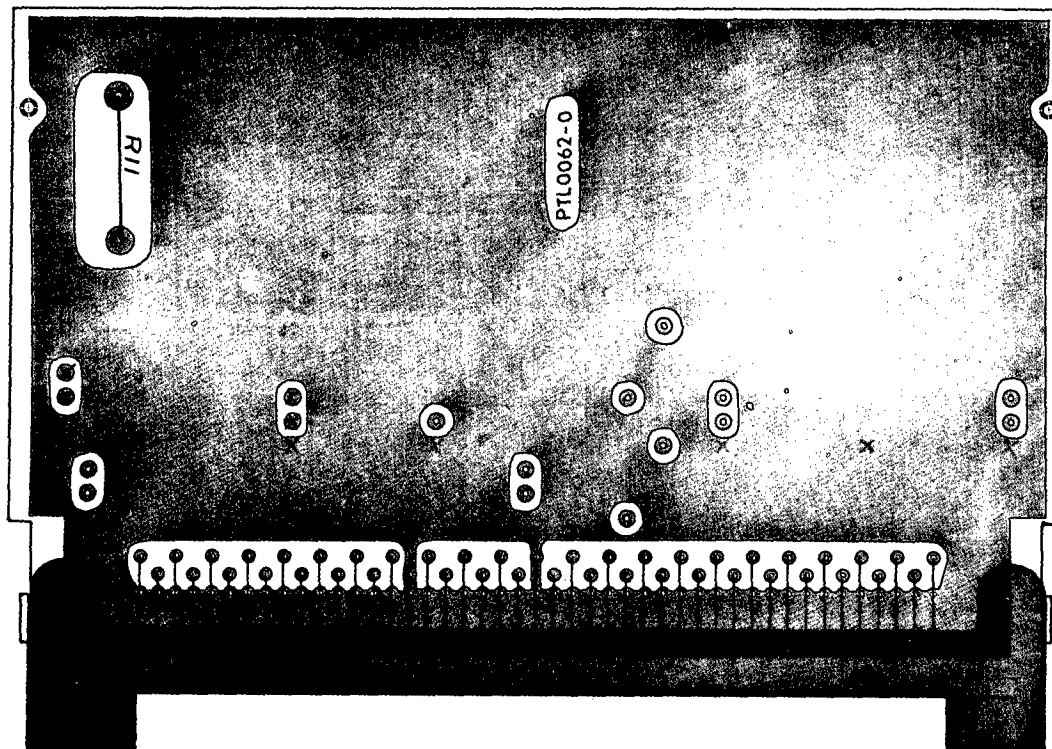
APPROVED			ENG	DRAWING NO.
BY	FOR	DATE	NTK	
<i>Maw</i>	<i>Prod.</i>	<i>7/28/70</i>	DRAWN BY	205.4D
<i>Cen</i>	<i>MANUF.</i>	<i>10-11-71</i>	KM	
			CHECKED	DATE 6-16-70
			<i>Maw</i>	



				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE REGISTER LATERAL MOTHERBOARD ASSEMBLY SIGNAL SIDE PART NO. 205.6					
						APPROVED BY <i>Cam</i> FOR <i>MANUF.</i> DATE <i>2-10-70</i>		ENG. <i>DLS</i> DRAWN BY <i>PLL</i>		DRAWING NO. 205.6D1	
1	11-9-70	E.C.O. 0079		MACROMODULAR PROJECT		CHECKED <i>Cam</i> DATE <i>9-13-71</i>		DATE NTK 4-17-70			
CHANGE NO.	DATE	DESCRIPTION									

NOTE:
AMPMODU PINS MUST BE INSTALLED
FROM THIS SIDE IN LOCATIONS MARKED
X PRECISELY AS SHOWN IN DRAWINGS
200.50D1 AND 200.50D2.

NOTE:
SEE DRAWING NUMBER 200.50D28
FOR CONNECTOR ORIENTATION.



COMPUTER SYSTEMS LABORATORY
WASHINGTON UNIVERSITY
ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE
COMPONENT IDENTIFICATION
REGISTER LATERAL MOTHERBOARD ASSEMBLY COMPONENT SIDE
PART NO. 205.6

APPROVED			ENG.	DRAWING NO.
BY	FOR	DATE	OLS	205.6D2
Cam	MANUF.	11-22-70	DRAWN BY PLL	
Cam	MANUF.	9-15-71	CHECKED NTK	DATE 11-10-70

CHANGE NO.	DATE	DESCRIPTION

JUMPERS
THREE REQUIRED

R1
R2
R4

RESISTOR 34K OHM 1% FILM
ONE REQUIRED
R03

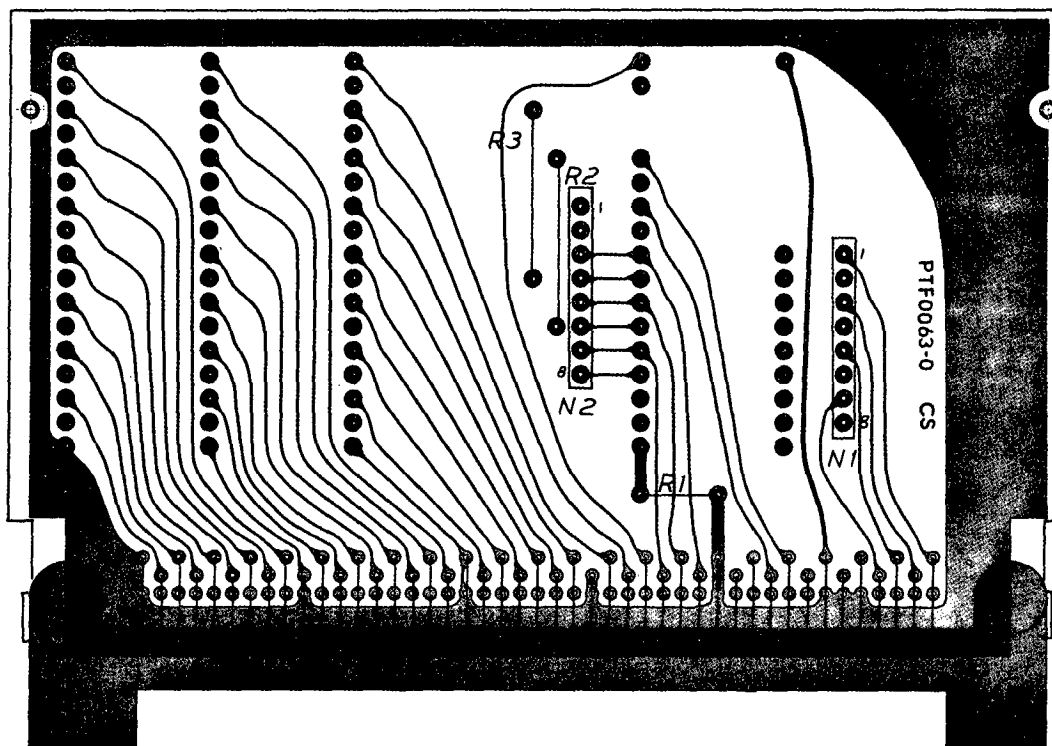
CONNECTOR AMP 583 464-1
ONE REQUIRED

CONNECTOR
AMP MODU NO. 85931-5
SIXTEEN REQUIRED

FUSE BUSSMAN GFA THREE-QUARTER AMP
ONE REQUIRED
R11

CIRCUIT BOARD
PTL0062-1
ONE REQUIRED

2	7-15-71	E.C.O. 0206
1	11-9-70	E.C.O. 0079 <i>NTK Com</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST REGISTER LATERAL MOTHER BOARD PART NO. 205.6		
APPROVED		ENG. <i>DLS</i>
BY	FOR	DATE
<i>Com</i>	MANUF.	<i>11-9-70</i>
<i>Com</i>	MANUF.	<i>9-15-71</i>
CHECKED <i>NTK</i>		DATE <i>11-9-70</i>
DRAWING NO.		205.6D3



NOTE:
MALE AMP MODU MUST BE
INSTALLED FROM THIS SIDE IN
LOCATIONS MARKED X PRECISELY
AS SHOWN IN DRAWINGS 200.50D1
AND 200.50D2.

NOTE:
SEE DRAWING 200.50D29 FOR
CONNECTOR ORIENTATION.

			COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION REGISTER FACEPLATE MOTHERBOARD ASSEMBLY PART NO. 205.7			
					APPROVED BY <i>cam</i> FOR <i>MANUF</i> DATE <i>9-15-71</i>			
1 11-9-70 E.C.O. 0079 <i>200K</i> <i>clm</i>			MACROMODULAR PROJECT		ENG. <i>DLS</i> DRAWN BY <i>PLL</i>		DRAWING NO. 205.7D1	
CHANGE NO. DATE DESCRIPTION					CHECKED <i>NTK</i>		DATE 4-17-70	

JUMPER
ONE REQUIRED
R1

RESISTOR 15K OHM 1/4WATT 5% CARBON COMP.
TWO REQUIRED
R2
R3

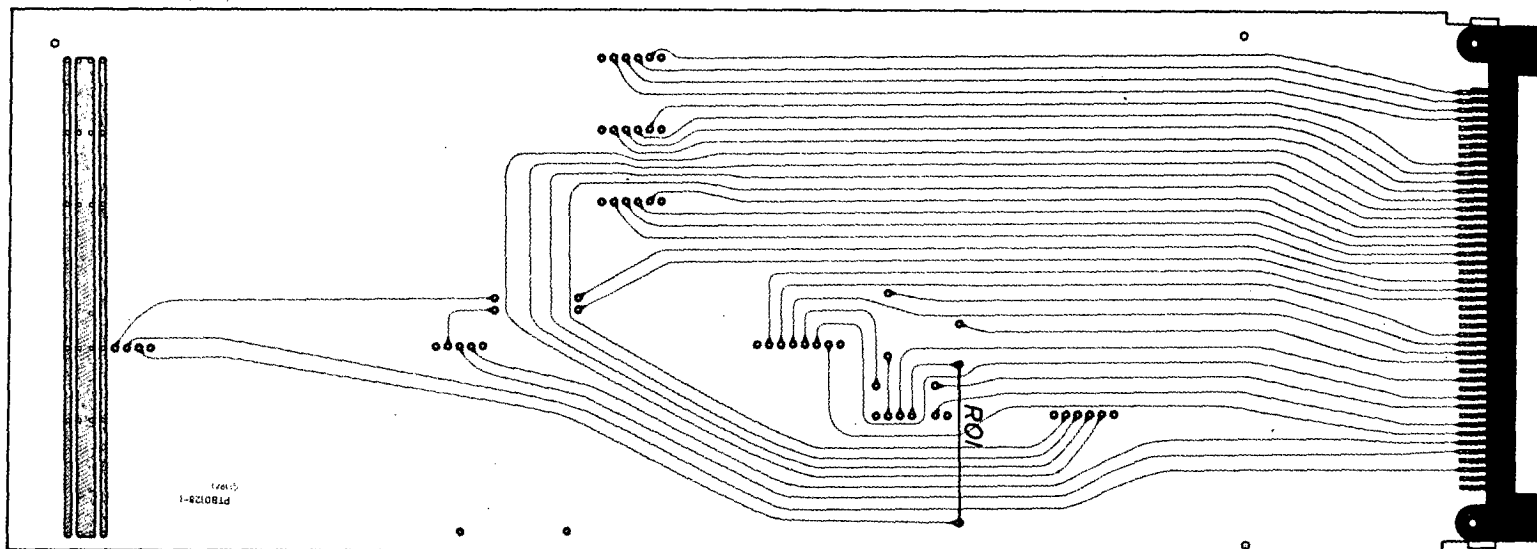
NETWORK LTN-2
TWO REQUIRED
N1
N2

CONNECTOR AMP 583 464-1
ONE REQUIRED

CONNECTOR
AMPMODU NO. 85931-5
EIGHTY-TWO REQUIRED

CIRCUIT BOARD
PTF0063-1
ONE REQUIRED

2	9-9-71	E.C.O. 0226
1	11-9-70	E.C.O. 0079 <i>NTR</i>
CHANGE NO.	DATE	DESCRIPTION
COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		
MACROMODULAR PROJECT		
TITLE PARTS LIST REGISTER FACEPLATE MOTHER BOARD PART NO. 205.7		
APPROVED		ENG. <i>DLS</i>
BY	FOR	DATE
	MANUF.	11-9-70
CHECKED		
		DATE
		11-9-70
DRAWING NO.		205.7D2



COMPUTER SYSTEMS LABORATORY

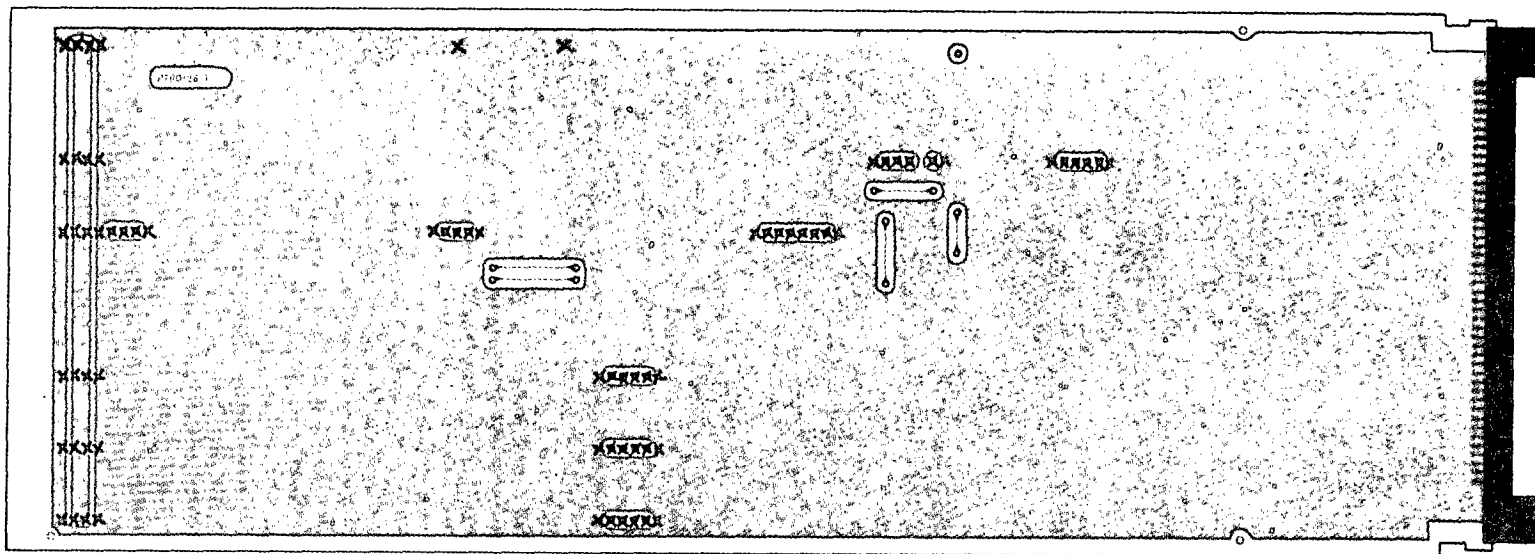
WASHINGTON UNIVERSITY
ST. LOUIS, MISSOURI

MACROMODULAR PROJECT

TITLE
COMPONENT IDENTIFICATION
REGISTER BOTTOM MOTHERBOARD ASSEMBLY SIGNAL SIDE
PART NO. 205.8

APPROVED			ENG. REO	DRAWING NO.
BY	FOR	DATE	DRAWN BY	205.8D1
CM	MANU.	9/15/70	MBP	
CHECKED			DATE	
MK			8-13-71	

CHANGE NO.	DATE	DESCRIPTION



NOTE 1:
SEE DRAWING 200.50D27 FOR
CONNECTOR ORIENTATION.

NOTE 2:
MALE AMPMODU PINS MUST BE INSTALLED FROM
THIS SIDE IN LOCATIONS MARKED "X" PRECISELY
AS SHOWN IN DRAWINGS 200.50D1 AND 200.50D2.
(73 PINS)

CHANGE NO.	DATE	DESCRIPTION	COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI	TITLE COMPONENT IDENTIFICATION REGISTER BOTTOM MOTHERBOARD ASSEMBLY COMPONENT SIDE PART NO. 205.8			
				APPROVED BY <i>CLM</i> FOR <i>FIAMU</i> DATE <i>9/15/71</i>		ENG. REO DRAWN BY MBP	DRAWING NO. 205.8D2
			MACROMODULAR PROJECT			CHECKED <i>WTR</i>	DATE 9-10-71

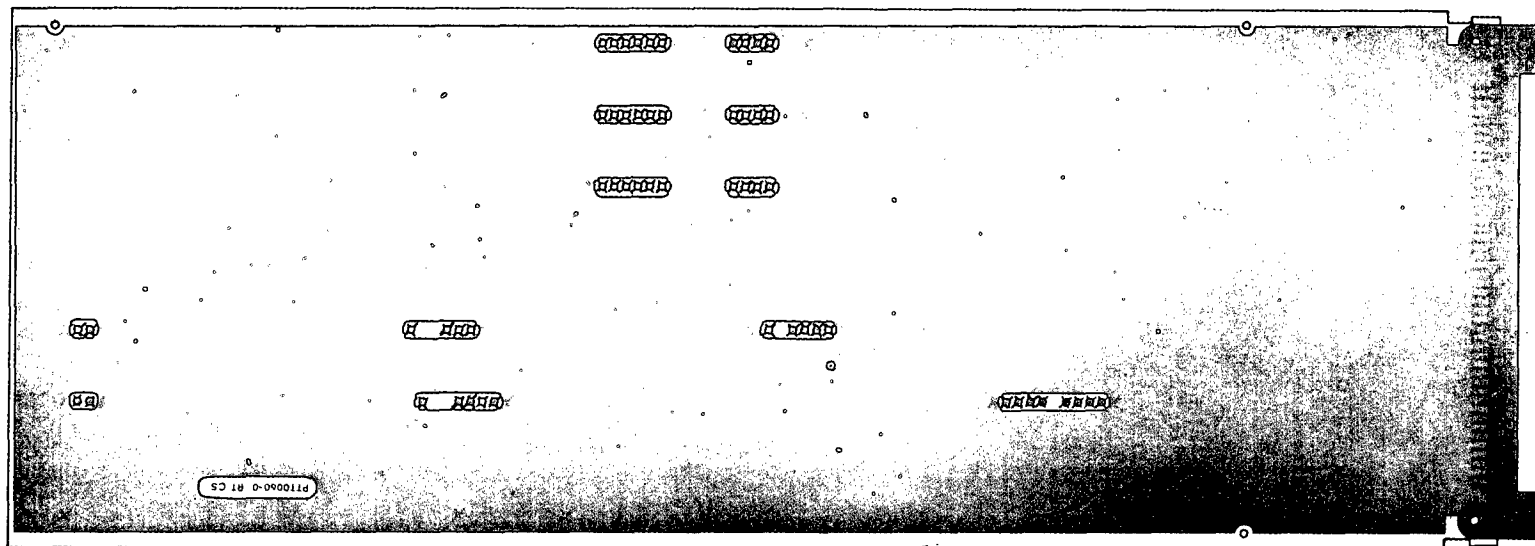
JUMPER
R01
ONE REQUIRED

CONNECTOR
AMP 1-202845-5
ONE REQUIRED

CONNECTOR
AMP NO. 85931-5
SEVENTY-THREE REQUIRED

CIRCUIT BOARD
PTB0128-1
ONE REQUIRED

CHANGE NO.	DATE	DESCRIPTION			
<p align="center">COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI</p>					
<p align="center">MACROMODULAR PROJECT</p>					
<p>TITLE PARTS LIST REGISTER BOTTOM MOTHER BOARD PART NO. 205.8</p>					
APPROVED				ENG. REO	DRAWING NO.
BY	FOR	DATE	DRAWN BY		205.8D3
COLM			MBP		
CHECKED				DATE	9-10-71



NOTE: SEE DRAWING NUMBER
200.50D26 FOR
CONNECTOR ORIENTATION.

NOTE:

MALE AMP MODU PINS MUST BE
INSTALLED FROM THIS SIDE IN
LOCATIONS MARKED X PRECISELY
AS SHOWN IN DRAWINGS 200.50D1
AND 200.50D2.

				COMPUTER SYSTEMS LABORATORY WASHINGTON UNIVERSITY ST. LOUIS, MISSOURI		TITLE COMPONENT IDENTIFICATION REGISTER TOP MOTHER BOARD ASSEMBLY PART NO. 205.9	
				MACROMODULAR PROJECT		APPROVED BY FOR DATE Cam MANUF. 21 APR 70 Cam MANUF. 9-15-71	
						ENG. DLS DRAWN BY PLL CHECKED NTK	
						DRAWING NO. 205.9D1 DATE 4-18-70	
CHANGE NO.	DATE	DESCRIPTION					

CONNECTOR AMP 1-202845-5
ONE REQUIRED

CONNECTOR
AMPMODU NO. 85931-5
EIGHTY-SEVEN REQUIRED

CIRCUIT BOARD
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ONE REQUIRED

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14. KEY WORDS	LINK A		LINK B		LINK C	
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REGISTER Macromodule						
Macromodule						
Emitter-Coupled Logic						
Asynchronous Logic						

